

# EE 508

## Lecture 24

# Integrator Design

TA-C Integrators  
Other Integrator Structures

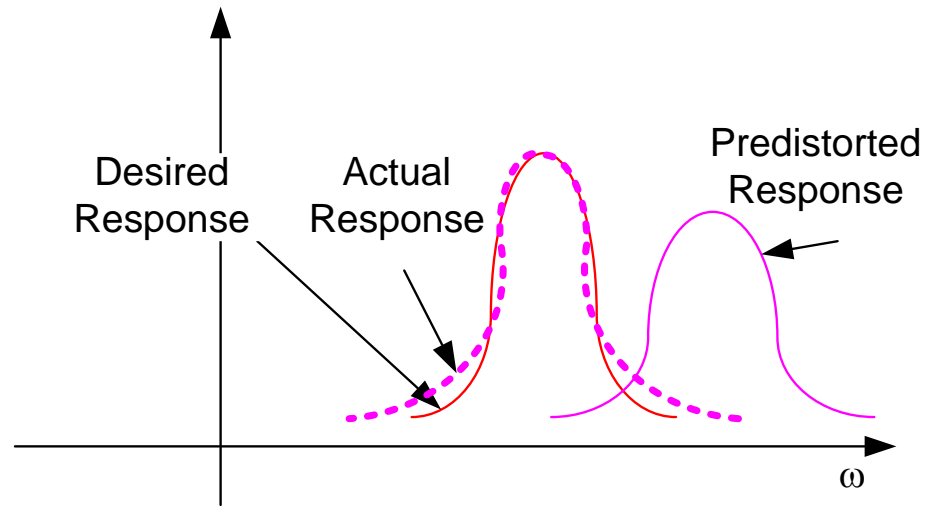
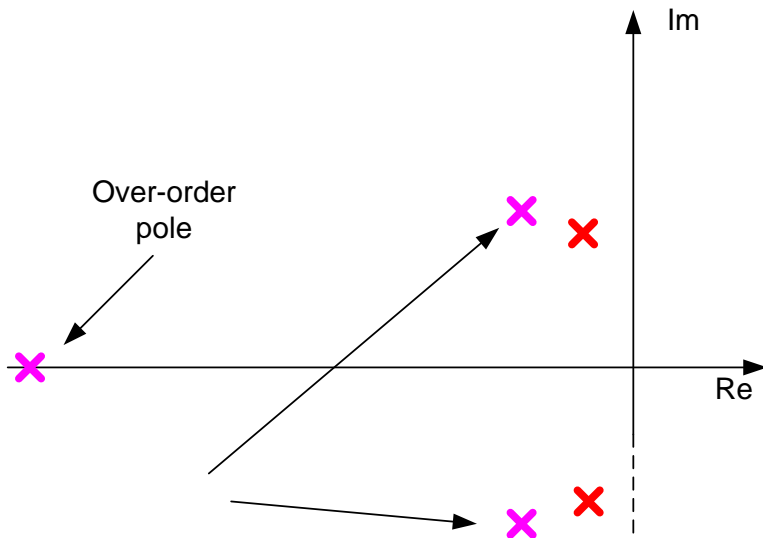
# Summary of Sensitivity Observations

- Sensitivity varies substantially from one implementation to another
- Variability too high, even with low sensitivity, for more demanding applications
- Methods of managing high variability
  - Select good structures
  - Trimming
    - Functional
    - Deterministic
  - Predistortion
    - In particular, for active sensitivities
    - Useful but not a total solution
  - Frequency Referenced Techniques
    - Master-Slave Control
      - Depends upon matching
      - Can self-trim or self-compensate
    - Switched-Capacitor Filters
    - AD/digital filter/D/A
  - Alternate Design Approach
    - Other methods

# What can be done to address these problems?

## 1. Predistortion

Design circuit so that after component shift, correct pole locations are obtained



Over-ordering Limitations with Pre-distortion

Parasitic Pole Affects Response

Predistortion almost always done even if benefits only modest

Not effective if significant deviations exist before predistortion

## What can be done to address these problems?

### 2. Trimming

#### a) Functional Trimming

- trim parameters of actual filter based upon measurements
- difficult to implement in many structures
- manageable for cascaded biquads

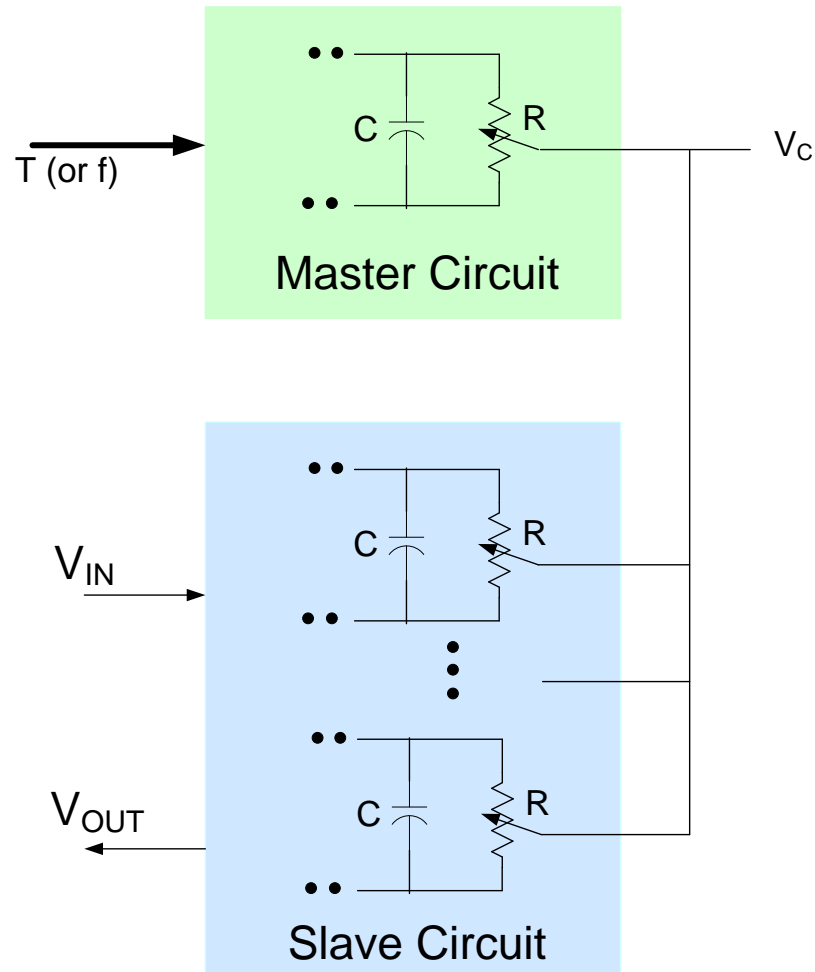
#### b) Deterministic Trimming (much preferred)

- Trim component values to their ideal value
  - Continuous-trims of resistors possible in some special processes
  - Continuous-trim of capacitors is more challenging
  - Link trimming of Rs or Cs is possible with either metal or switches
- If all components are ideal, the filter should also be ideal
  - R-trimming algorithms easy to implement
  - Limited to unidirectional trim
  - Trim generally done at wafer level for laser trimming, package for link trims
- Filter shifts occur due to stress in packaging and heat cycling

#### c) Master-slave reference control (depends upon matching in a process)

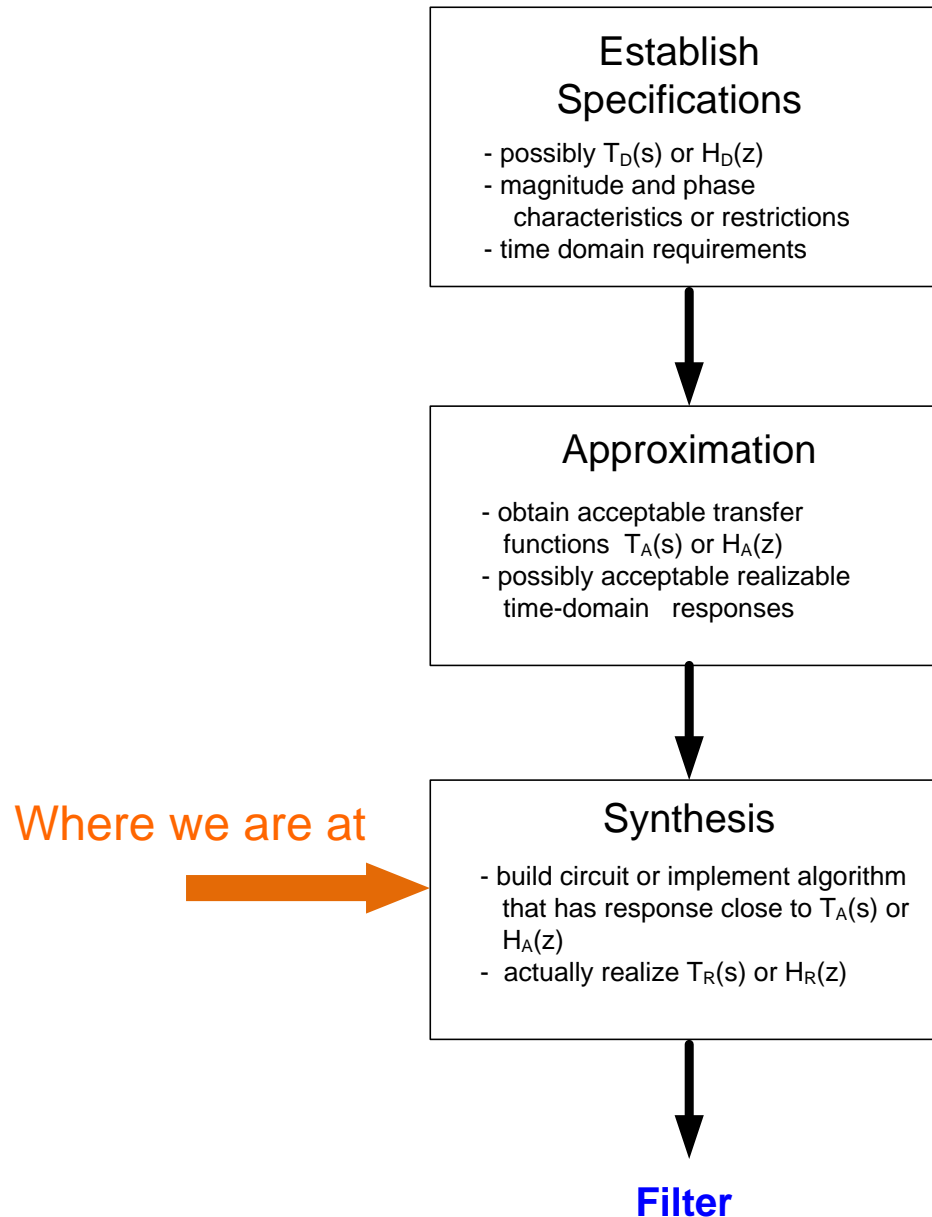
- Can be implemented in discrete or integrated structures
- Master typically frequency or period referenced
- Most effective in integrated form since good matching possible
- Widely used in integrated form

## Master-slave Control (depends upon matching in a process)



- Automatically adjust  $R$  (or  $C$ ) in the Master Circuit to match  $RC$  to  $T$
- Rely on matching to match  $RC$  products in Slave Circuit to  $T$
- Matching can be very good (1% or 0.1% or better)
- But does nothing to compensate for local random variations

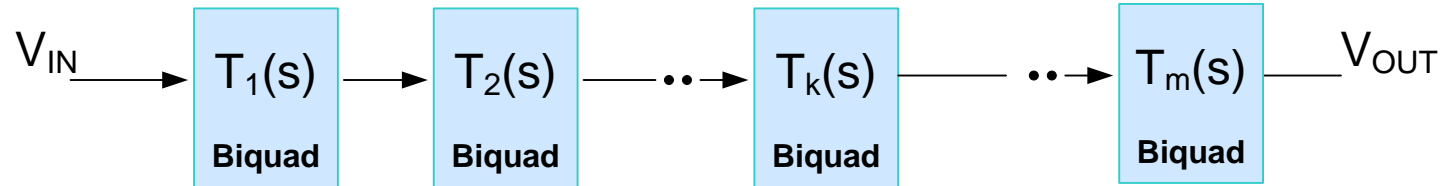
# Filter Design Process



# Filter Design/Synthesis Considerations

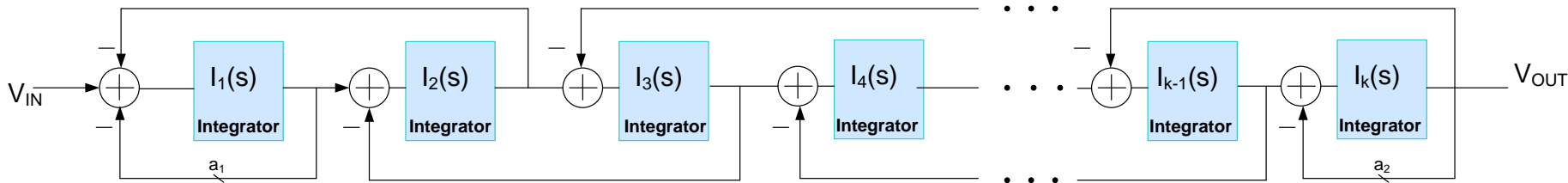
Most designs today use one of the following three basic architectures

## Cascaded Biquads

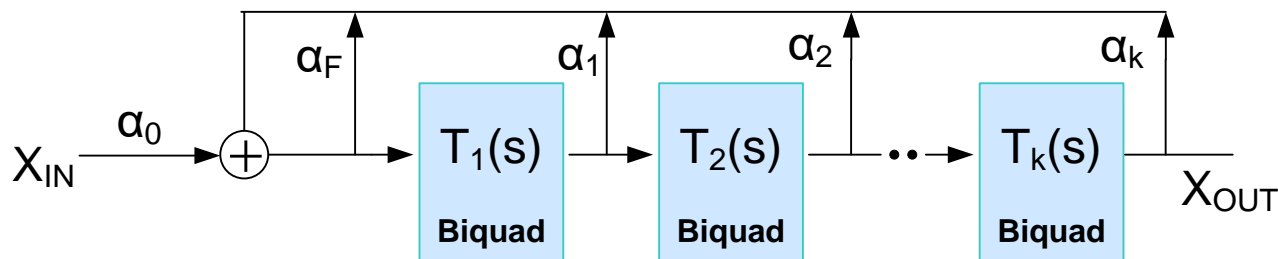


$$T(s) = T_1 T_2 \dots T_m$$

## Leapfrog

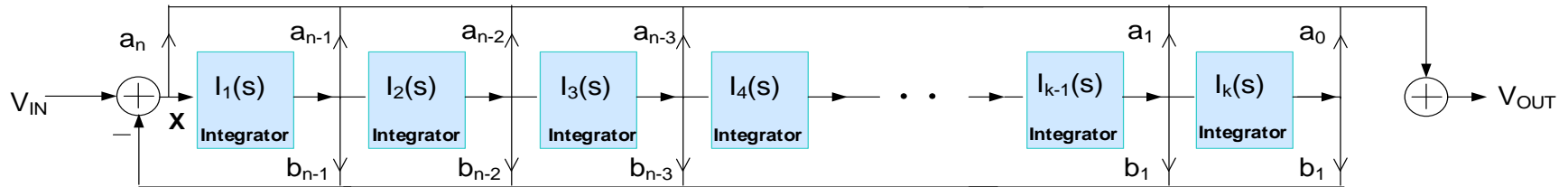


## Multiple-loop Feedback – One type shown (less popular)



# Filter Design/Synthesis Considerations

## Multiple-loop Feedback – Another type



$$X = V_{IN} - X \bullet \sum_{k=1}^n b_{n-k} \left( \frac{I_0}{s} \right)^k$$

$$V_{OUT} = X \bullet \sum_{k=0}^n a_{n-k} \left( \frac{I_0}{s} \right)^k$$

$$T(s) = \frac{\sum_{k=0}^n a_{n-k} \left( \frac{I_0}{s} \right)^k}{1 + \sum_{k=1}^n b_{n-k} \left( \frac{I_0}{s} \right)^k}$$

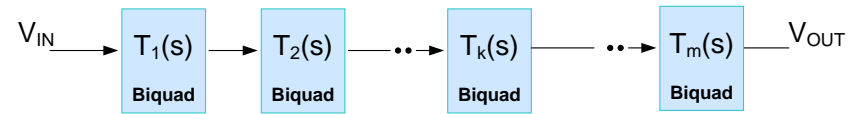
$$T(s) = \frac{\sum_{k=0}^n a_{n-k} I_0^k s^{n-k}}{s^n + \sum_{k=1}^n b_{n-k} I_0^k s^{n-k}}$$

- Termed the direct synthesis method
- Directly implements the coefficients in the numerator and denominator
- Approach followed in the Analog Computers
- Not particularly attractive from an overall performance viewpoint



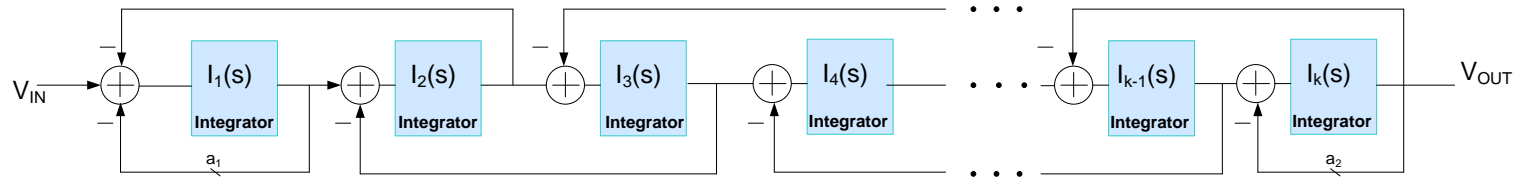
# Filter Design/Synthesis Considerations

## Cascaded Biquads

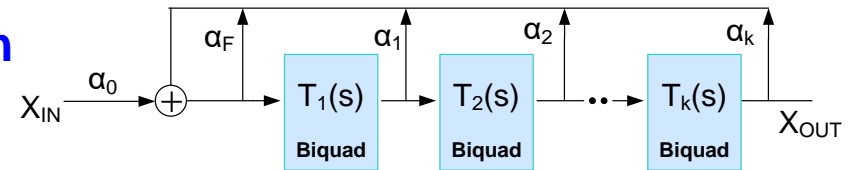


$$T(s) = T_1 T_2 \dots T_m$$

## Leapfrog



## Multiple-loop Feedback – One type shown



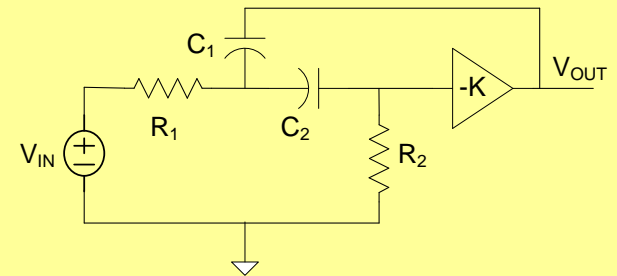
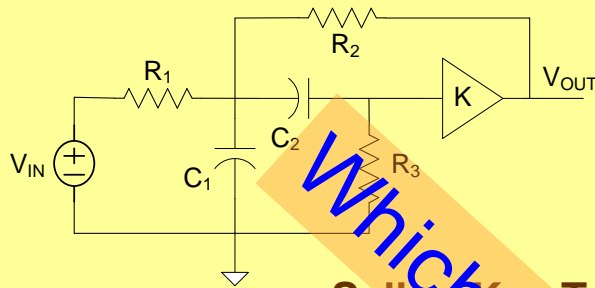
Will study details of all three types of architectures later

Observation: All filters are comprised of summers, biquads and integrators

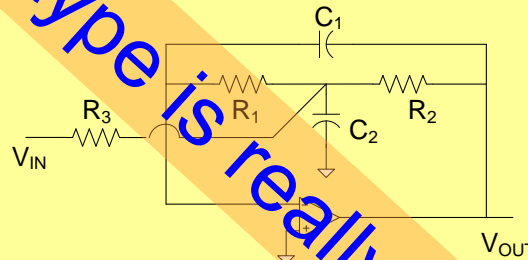
Consider now the biquads

# Biquad Filters Design Considerations

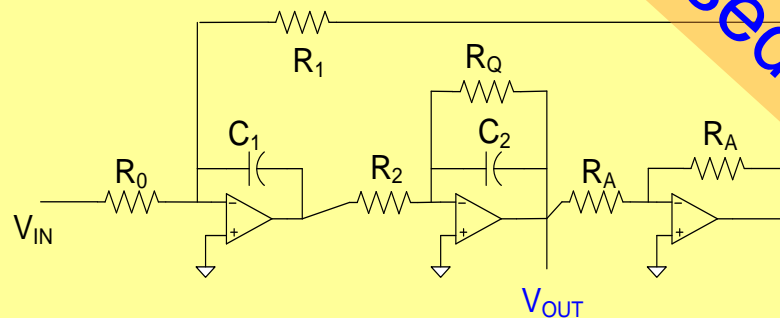
Several different Biquads were considered and other implementations exist



**Sallen-Key Type (Dependent Sources)**



**Infinite Gain Amplifiers**



**Integrator Based Structures**

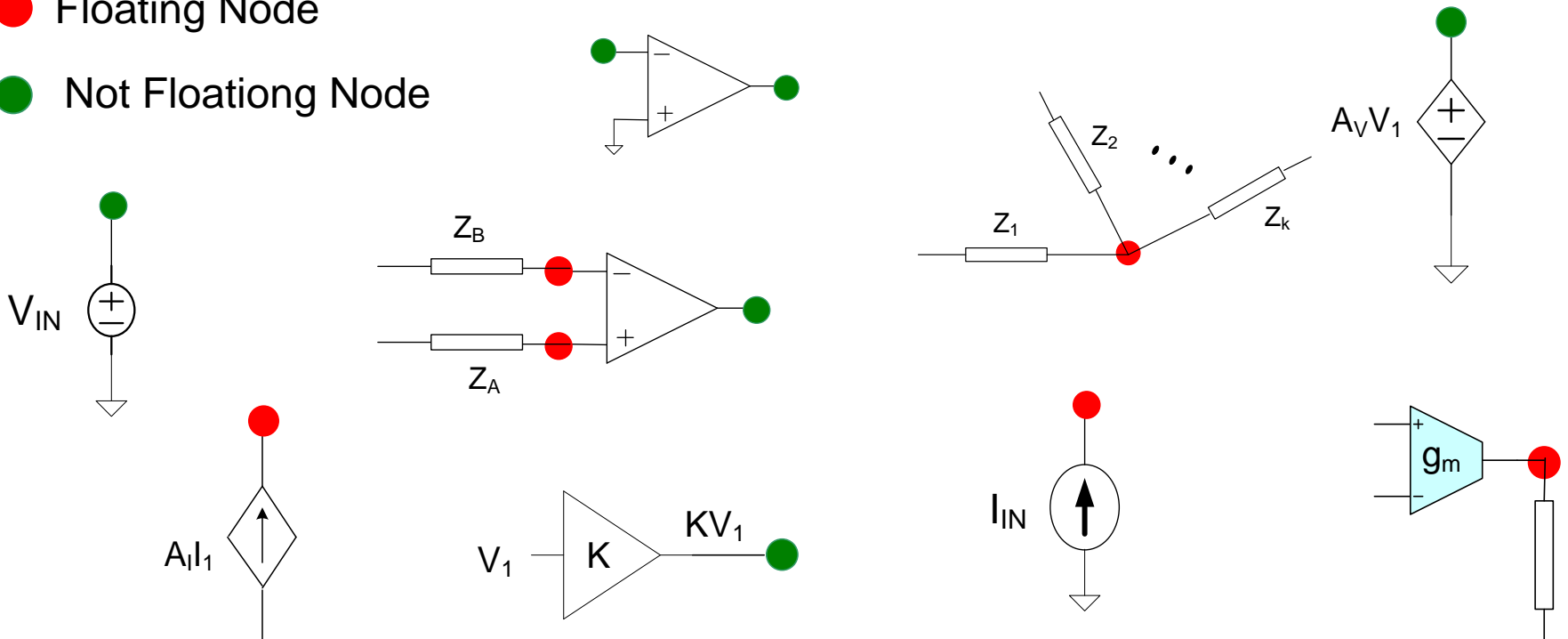
Which type is really used?

# Floating Nodes

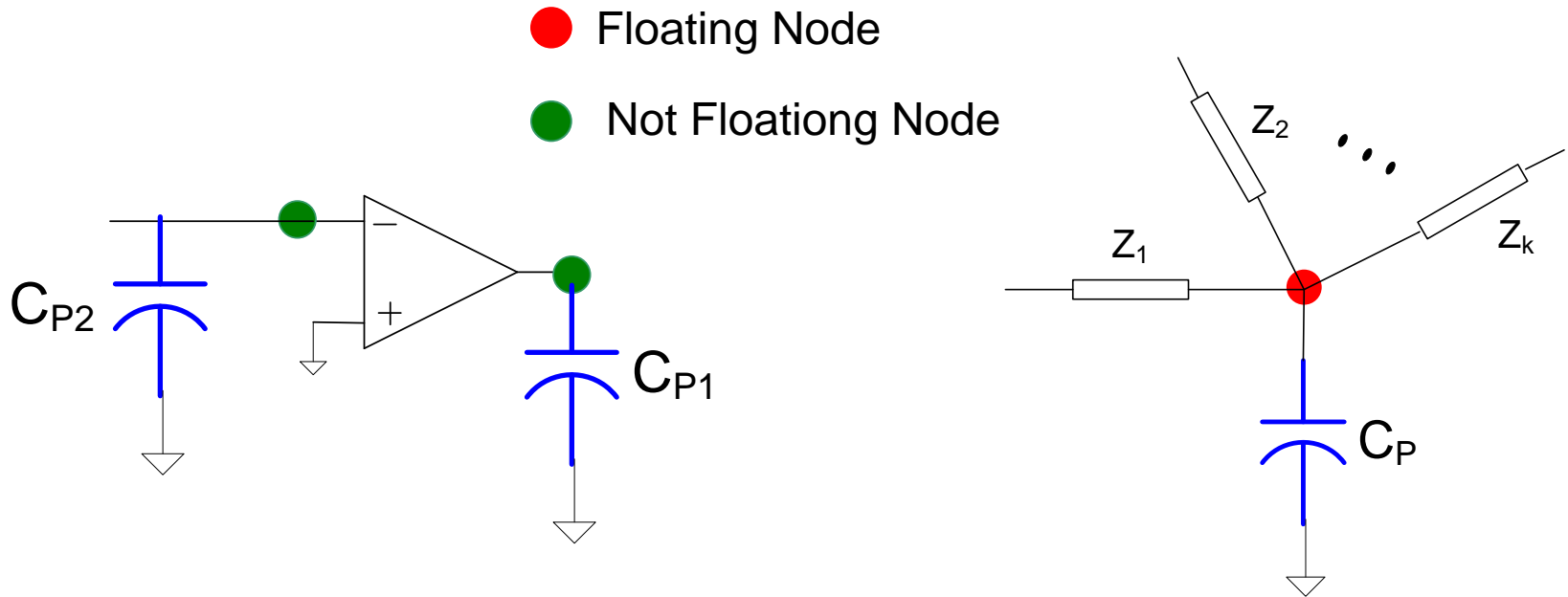
A node in a circuit is termed a **floating node** if it is not an output node of a ground-referenced voltage-output amplifier (dependent or independent), not connected to a ground-referenced voltage source, or not connected to a ground-referenced null-port

● Floating Node

● Not Floating Node



# Parasitic Capacitances on Floating Nodes



Parasitic capacitances ideally have no effect on filter when on a non-floating node but directly affect transfer function when they appear on a floating node

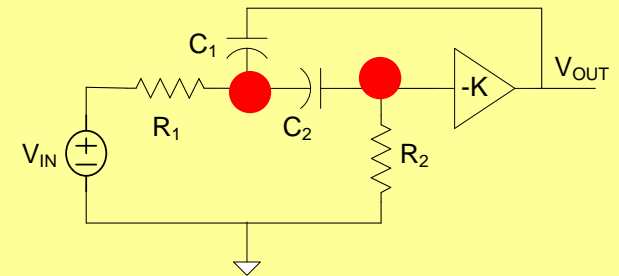
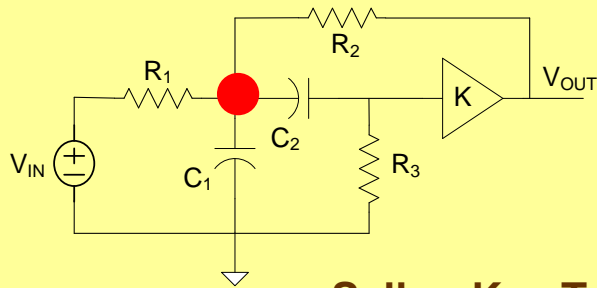
Parasitic capacitances are invariably large, nonlinear, and highly process dependent in integrated filters. Thus, it is difficult to build accurate integrated filters if floating nodes are present

Generally avoid floating nodes, if possible, in integrated filters

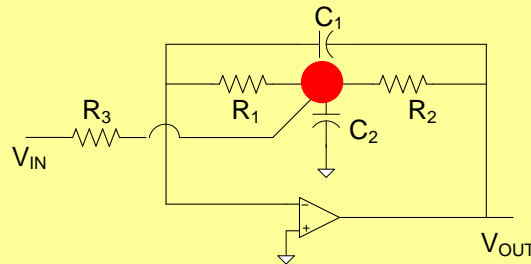
# Which type of Biquad is really used?

● Not Floating Node

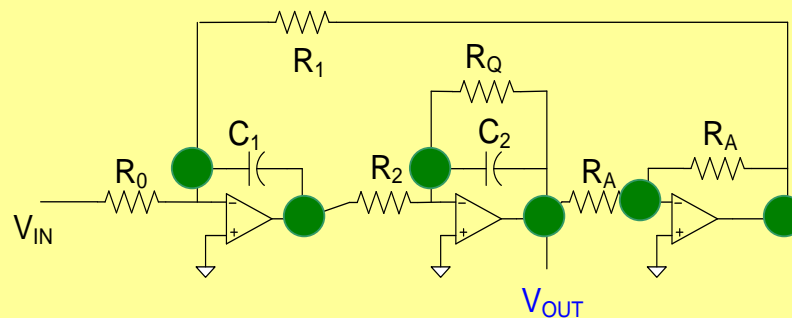
● Floating Node



**Sallen-Key Type (Dependent Sources)**



**Infinite Gain Amplifiers**



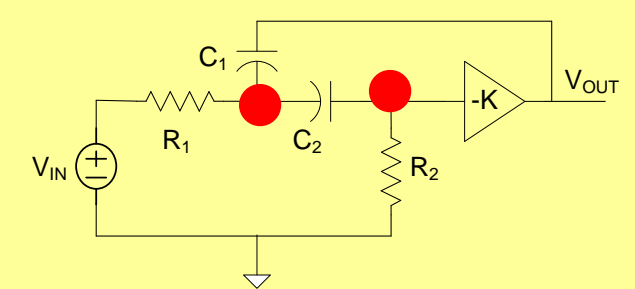
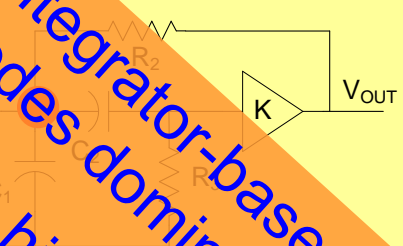
**Integrator Based Structures**

# Which type of Biquad is really used?

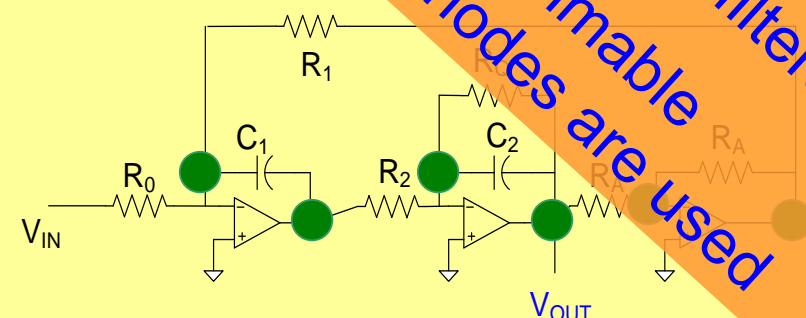
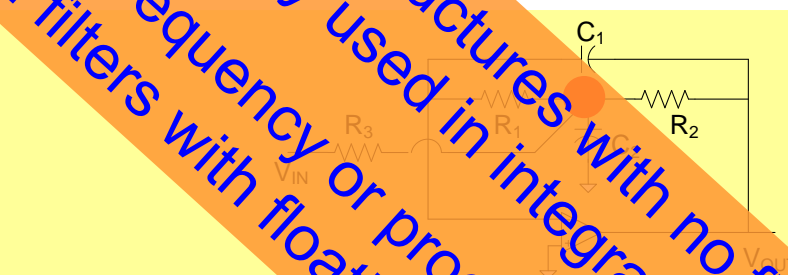
● Not Floating Node

● Floating Node

Integrator-based structures with no floating nodes dominantly used in integrated filters with floating nodes are used

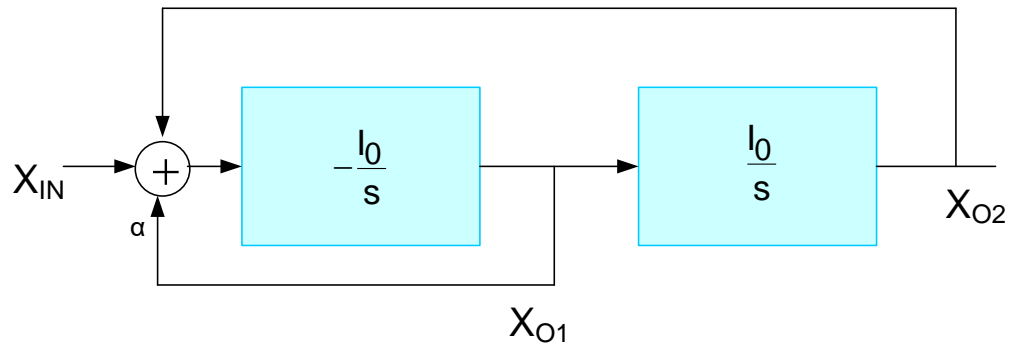
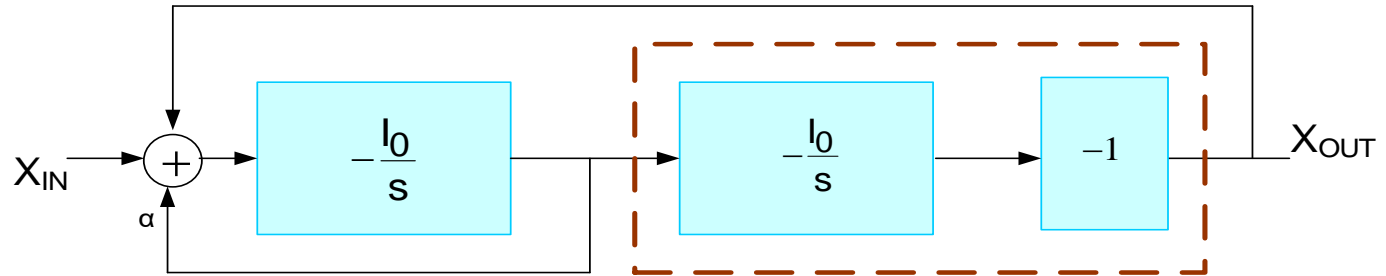
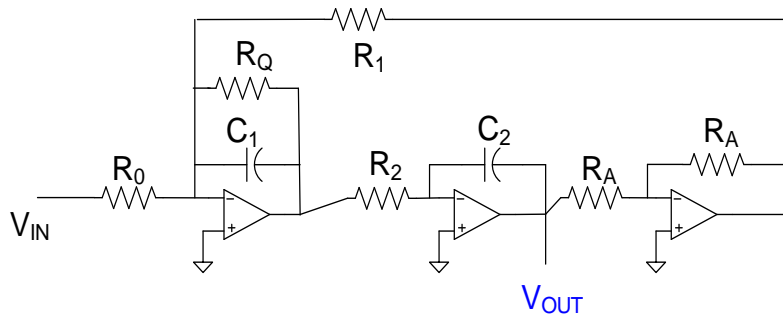


(Dependent Sources)

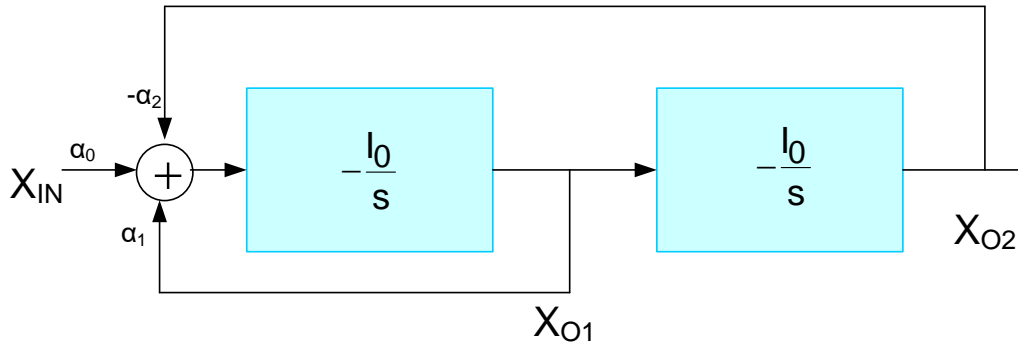


Integrator Based Structures

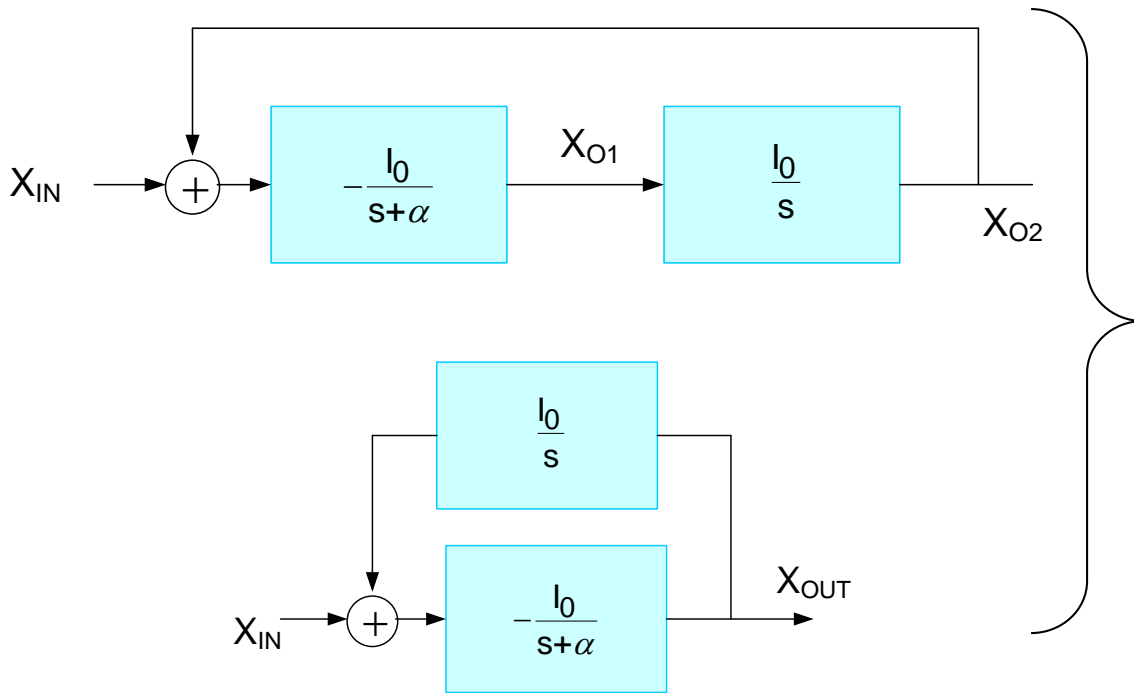
# Integrator-based Biquads



# Integrator-based Biquads



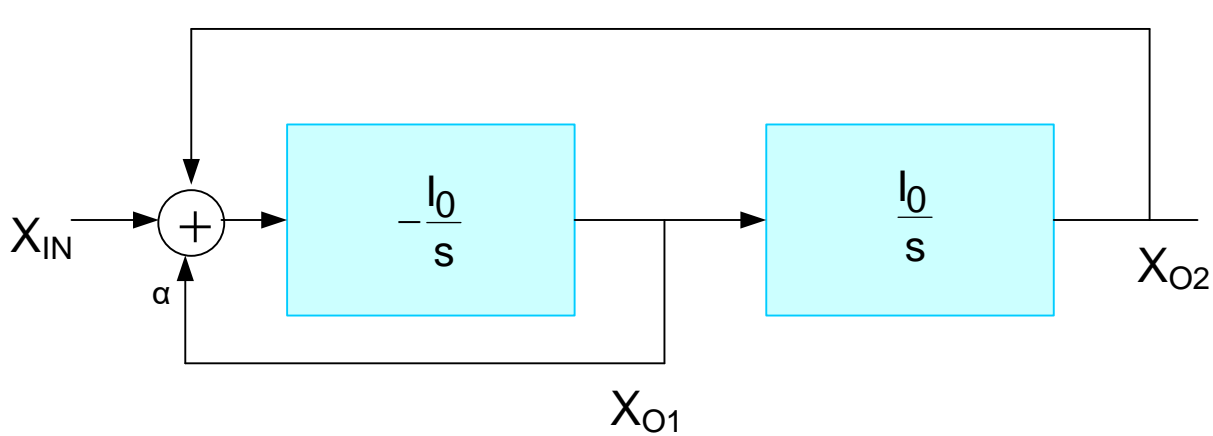
State Variable Biquad  
(Alt KHN Biquad)



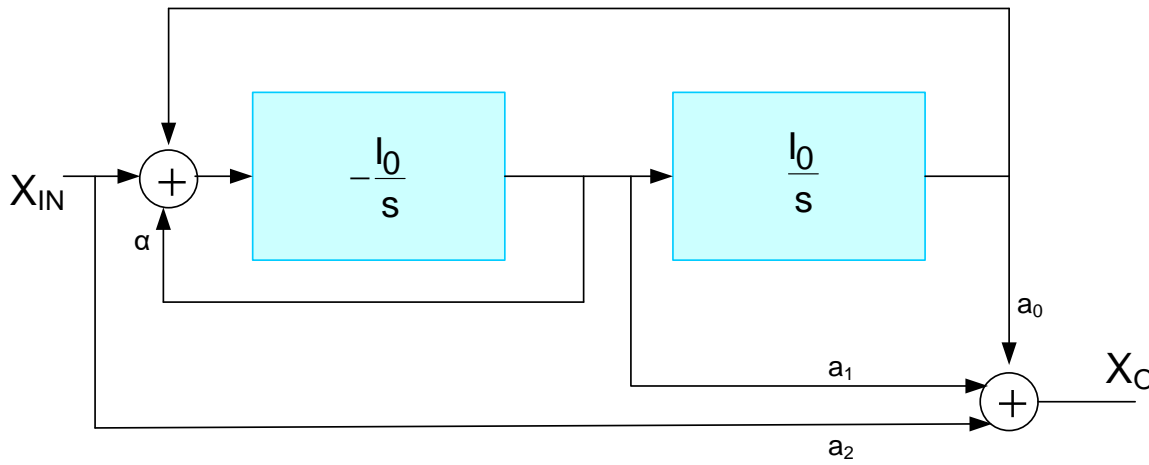
Integrator and lossy  
integrator in a loop



# Integrator-based Biquads

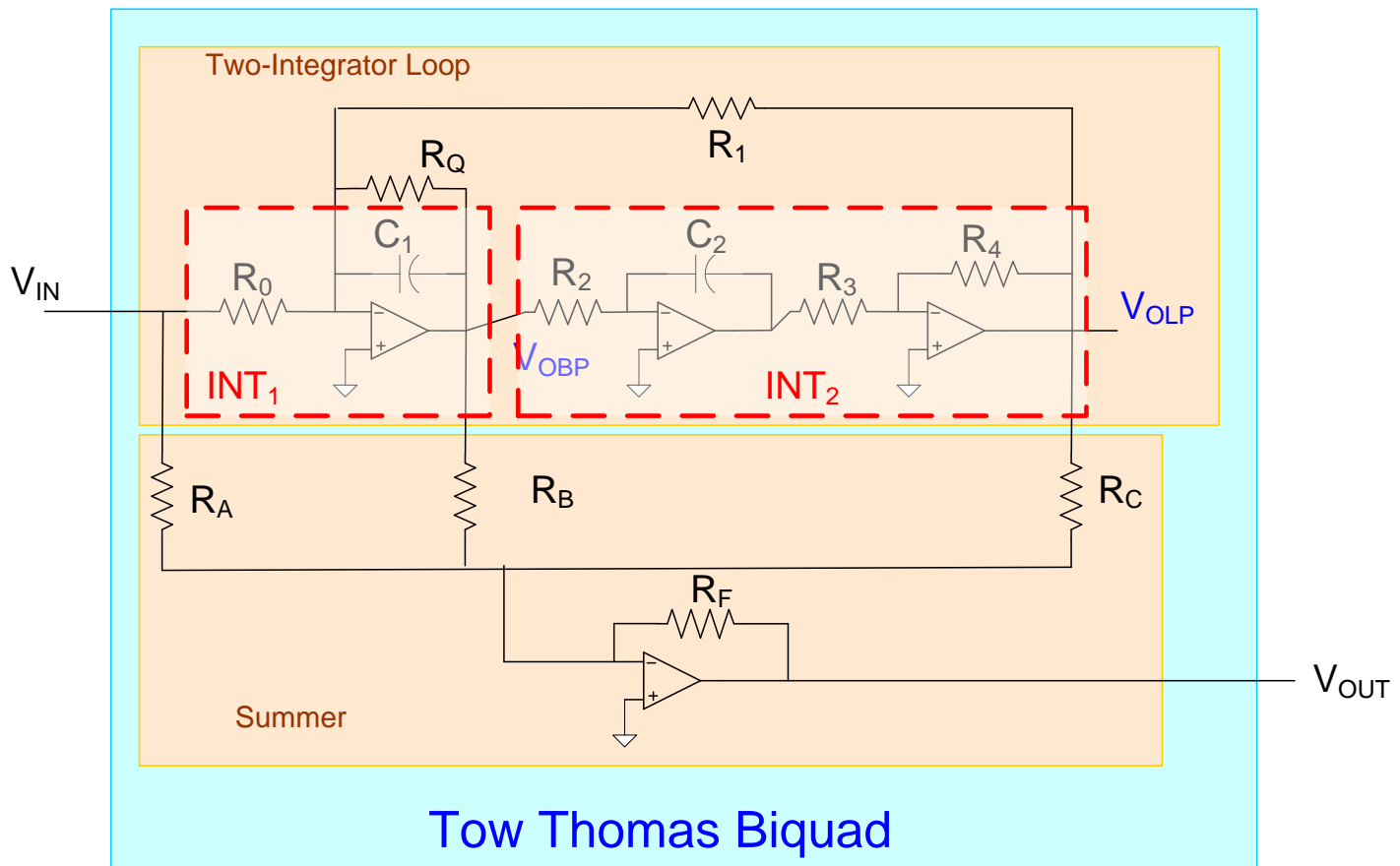
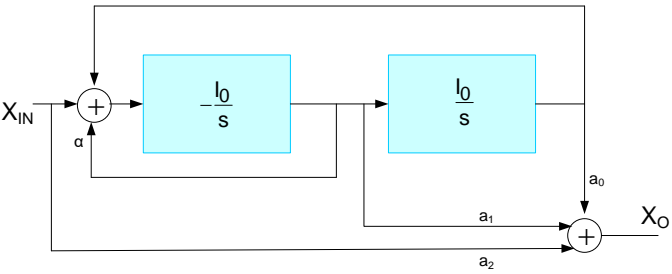


Tow-Thomas Biquad



With arbitrary zero locations

# Integrator-based Biquads

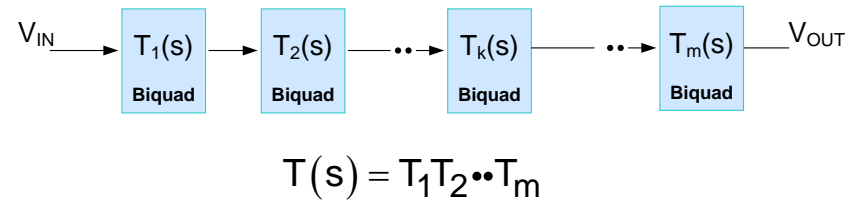


# Integrator-based Biquads

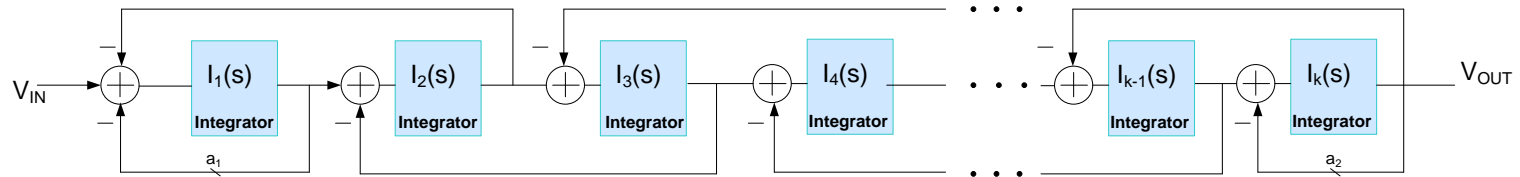
- Integrator-based biquads all involve two integrators in a loop
- All integrator-based biquads discussed have no floating nodes
- Most biquads in integrated filters are based upon two integrator loop structures
- The summers are usually included as summing inputs on the integrators
- The loss can be combined with the integrator to form a lossy integrator
- Performance of the minor variants of the two integrator loop structures are comparable

# Filter Design/Synthesis Considerations

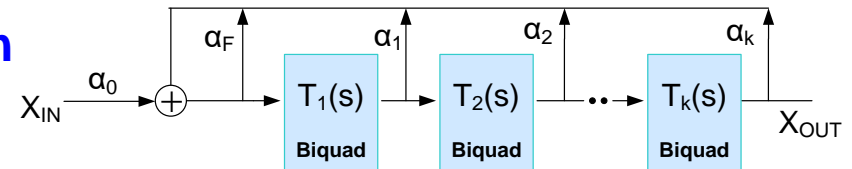
## Cascaded Biquads



## Leapfrog



## Multiple-loop Feedback – One type shown



Observation: All filters are comprised of summers, biquads and integrators

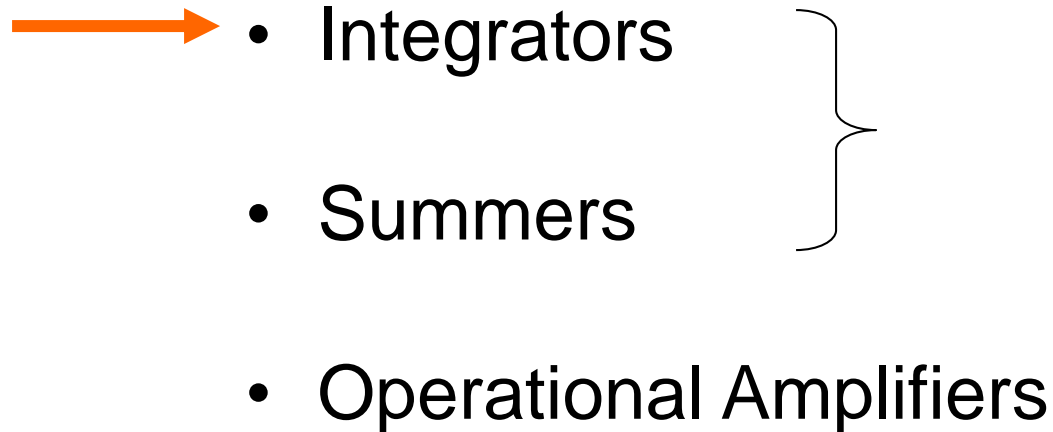
And biquads usually made with summers and integrators

Integrated filter design generally focused on design of integrators, summers, and amplifiers (Op Amps)

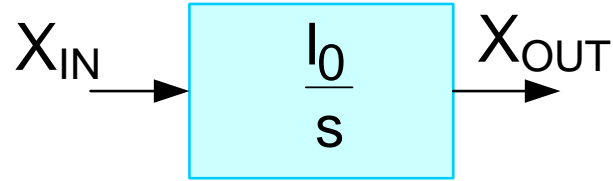
**Will now focus on the design of integrators, summers, and op amps**

# Basic Filter Building Blocks

(particularly for integrated filters)

- • Integrators
  - Summers
  - Operational Amplifiers
- 

# Integrator Characteristics of Interest



$$I(s) = \frac{I_0}{s}$$

Properties of an ideal integrator:

$$|I(j\omega)| = \frac{I_0}{\omega}$$

Gain decreases with  $1/\omega$

$$\angle I(j\omega) = -90^\circ$$

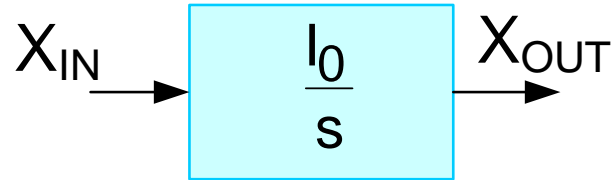
Phase is a constant  $-90^\circ$

$$|I(jI_0)| = 1$$

Unity Gain Frequency =  $I_0$

How important is it that an integrator have all 3 of these properties?

# Integrator Characteristics of Interest



$$I(s) = \frac{I_0}{s}$$

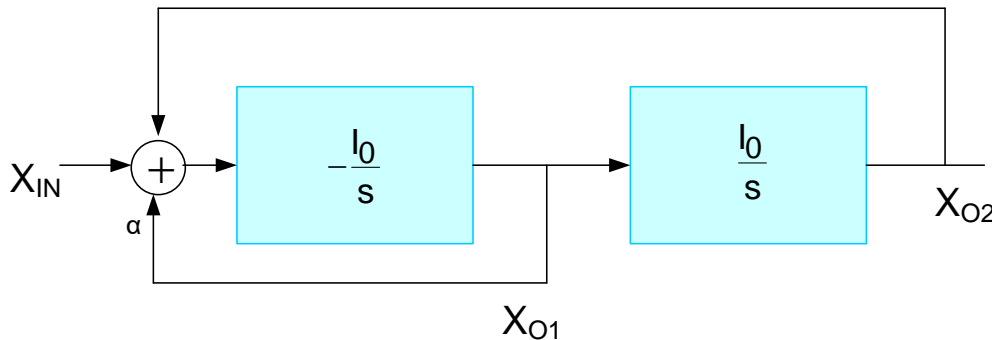
$$|I(j\omega)| = \frac{I_0}{\omega}$$

$$\angle I(j\omega) = -90^\circ$$

$$|I(jI_0)| = 1$$

How important is it that an integrator have all 3 of these properties?

Consider a filter example:



$$T(s) = \frac{-I_0^2}{s^2 + \alpha I_0 s + I_0^2}$$

$$Q = \frac{1}{\alpha} \quad \omega_0 = I_0$$

Band edges proportional to  $I_0$

Phase critical to make Q expression valid

**In many (most) applications it is critical that an integrator be very nearly ideal**  
(in the frequency range of interest)

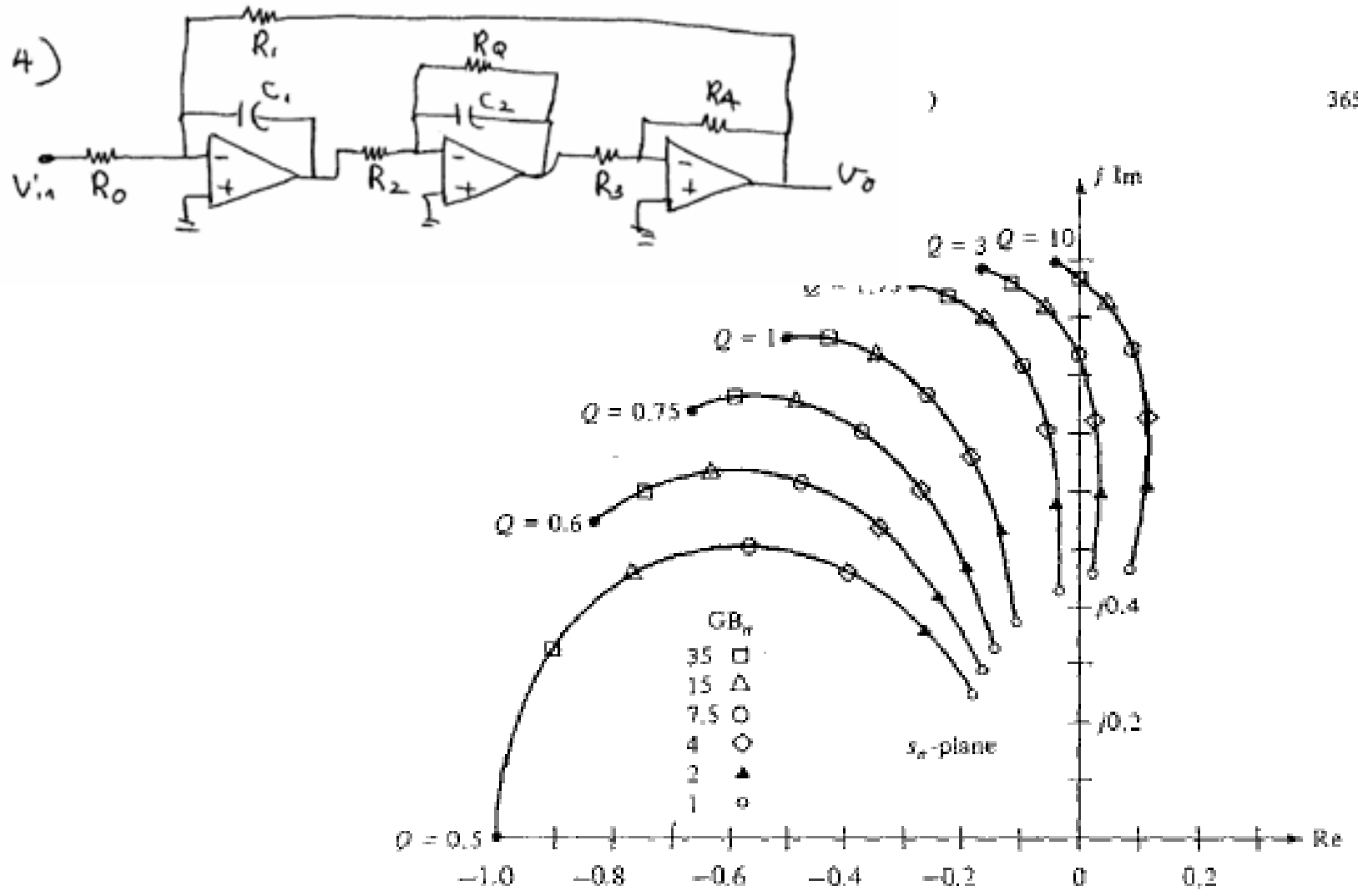
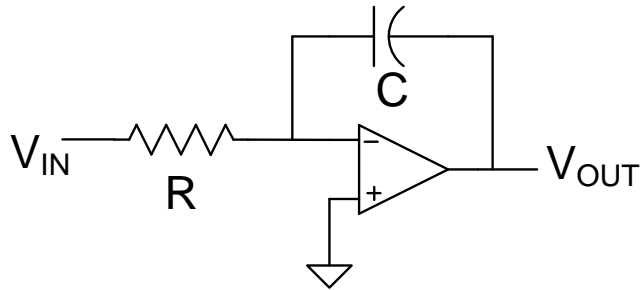


Fig. 10-17 Plot of upper half-plane root of

$$s^2 + s^2 \left( \frac{1}{2} + \frac{1}{Q} + \frac{GB_w}{4} \right) + s \frac{1}{4Q} (1 + GB_w) + \frac{GB_w}{4} = 0$$



# Some integrator structures

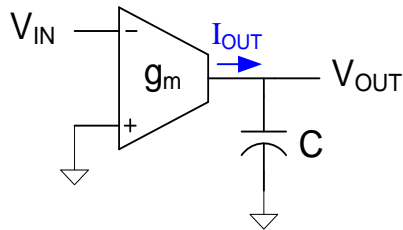


$$I(s) = -\frac{1}{RCs}$$

$$I_0 = \frac{1}{RC}$$

Inverting Active RC Integrator

Are there other integrator structures?

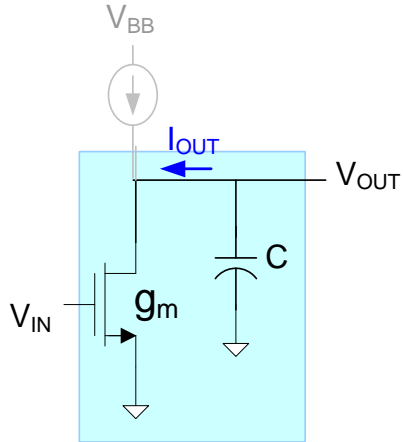


$$\left. \begin{aligned} I_{\text{OUT}} &= -g_m V_{\text{IN}} \\ V_{\text{OUT}} &= I_{\text{OUT}} \frac{1}{sC} \end{aligned} \right\} I(s) = -\frac{g_m}{sC} \quad I_0 = \frac{g_m}{C}$$

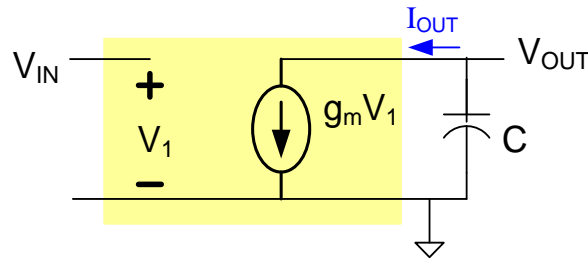
Termed an OTA-C or a gm-C integrator

# Some integrator structures

Are there other integrator structures?

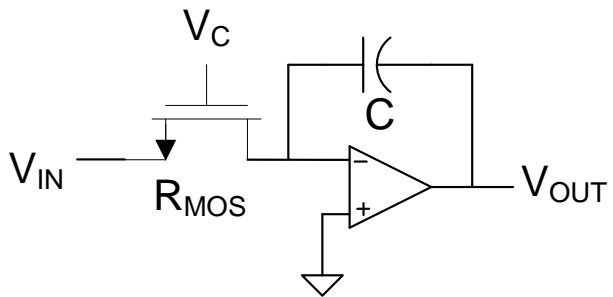


Termed a TA-C integrator



$$\left. \begin{aligned} I_{OUT} &= g_m V_{IN} \\ V_{OUT} &= -I_{OUT} \frac{1}{sC} \end{aligned} \right\} I(s) = -\frac{g_m}{sC}$$

$$I_0 = \frac{g_m}{C}$$



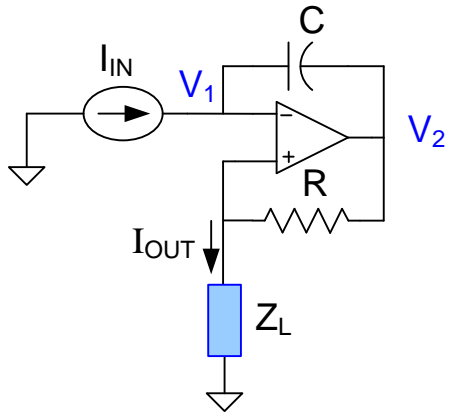
Termed MOSFET-C integrator

$$I(s) = -\frac{1}{sCR_{MOS}}$$

$$I_0 = -\frac{1}{R_{FET}C}$$

# Some integrator structures

Are there other integrator structures?



$$\left. \begin{aligned} V_2 &= V_1 - I_{IN} \frac{1}{sC} \\ I_{OUT} &= \frac{V_2 - V_1}{R} \end{aligned} \right\}$$

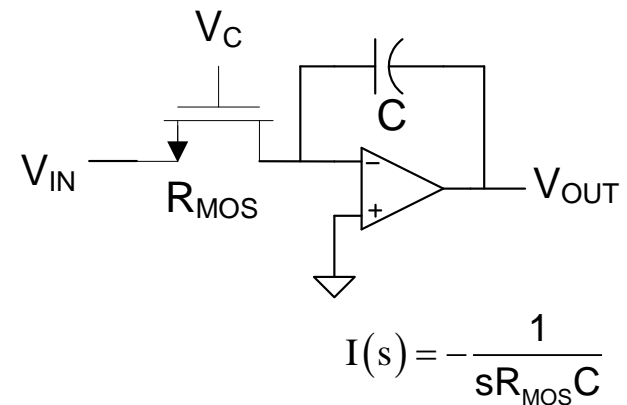
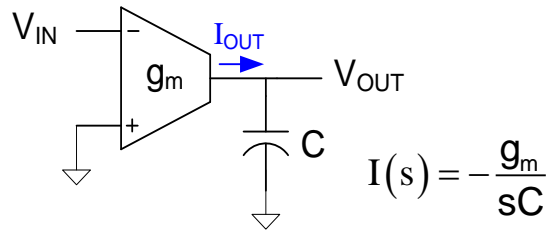
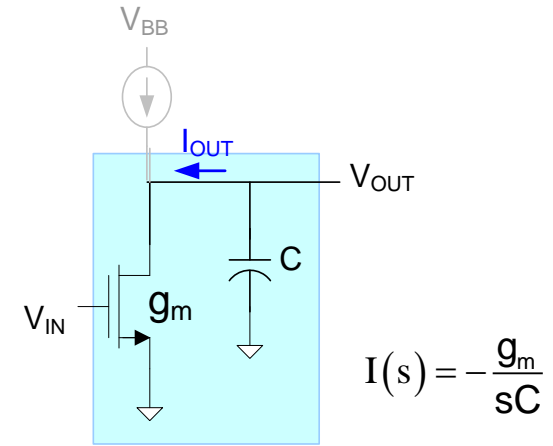
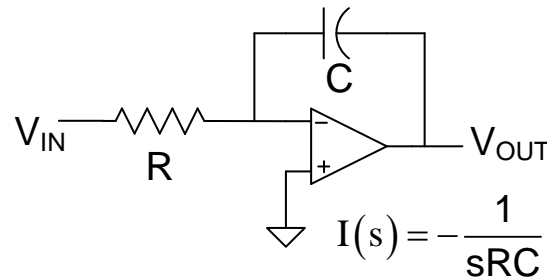
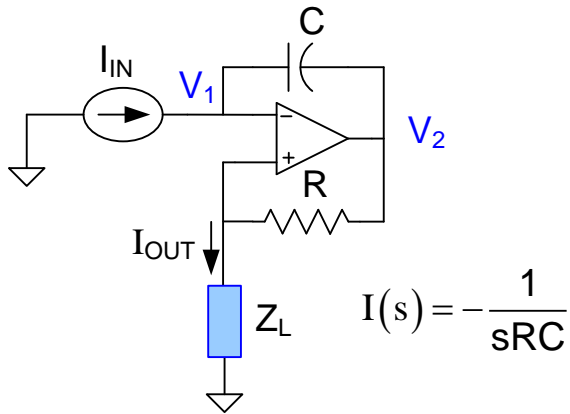
$$I(s) = \frac{I_{OUT}}{I_{IN}} = -\frac{1}{sRC}$$

$$I_0 = \frac{1}{RC}$$

- Output current is independent of  $Z_L$
- Thus output impedance is  $\infty$  so provides current output

Termed active RC current-mode integrator

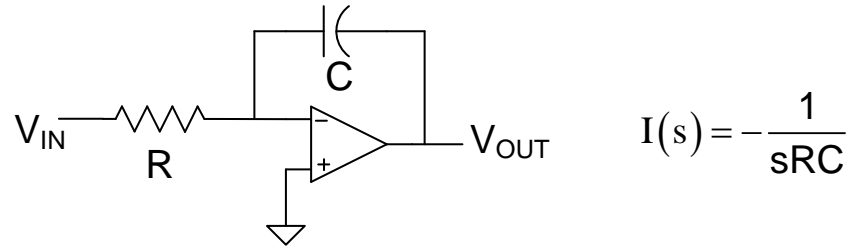
# Some integrator structures



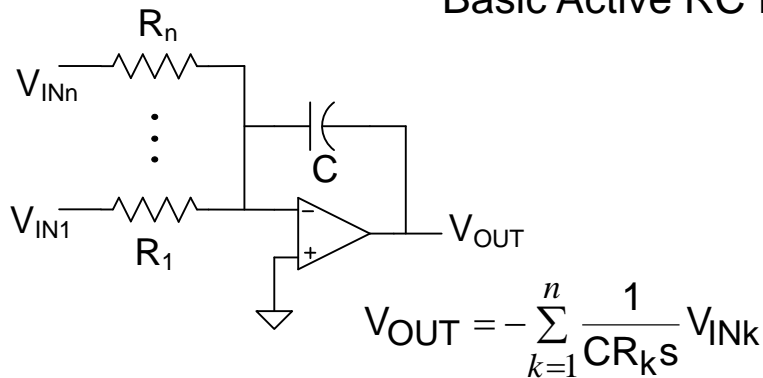
There are other useful integrator structures (some will be introduced later)

There are many different ways to build an inverting integrator

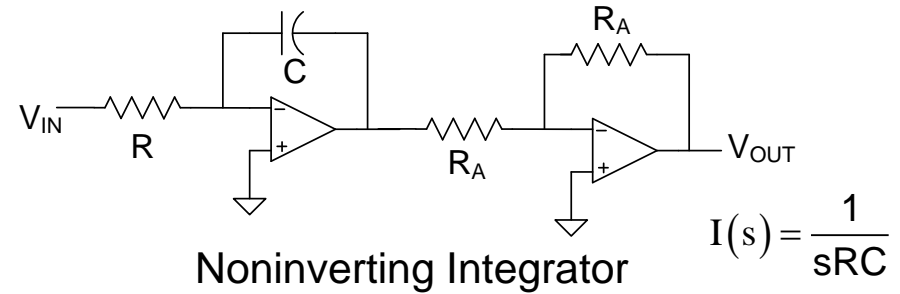
# Integrator Functionality



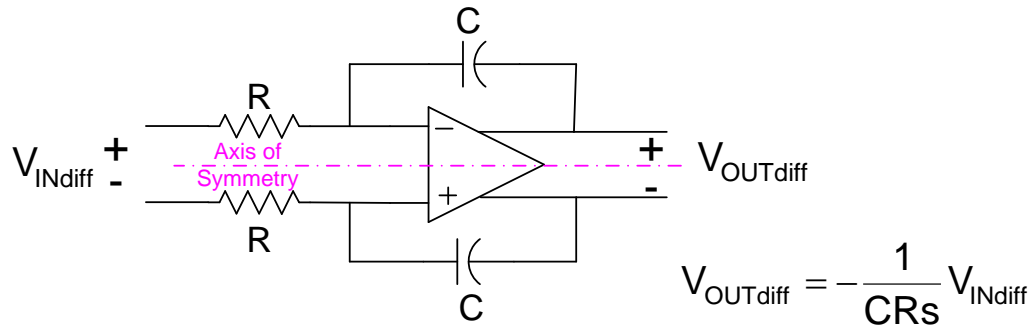
Basic Active RC Inverting Integrator



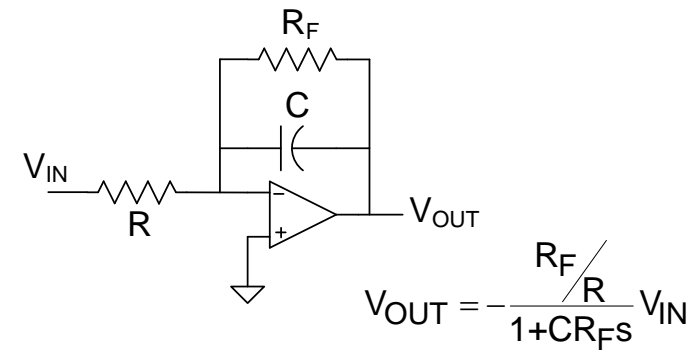
Summing Integrator



Noninverting Integrator



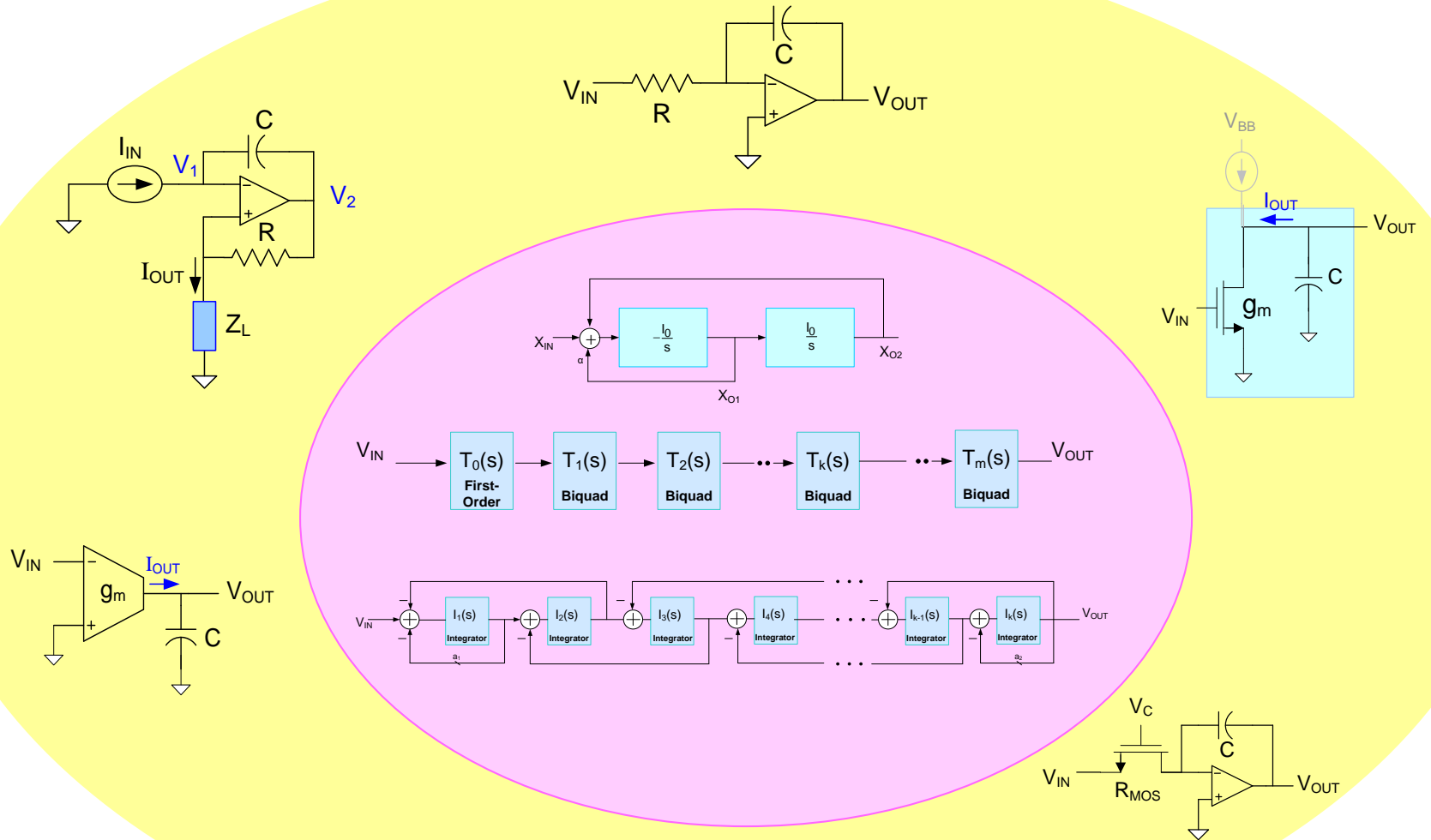
Fully Differential Integrator



Lossy Integrator

Many different types of functionality from basic inverting integrator  
Same modifications exist for other integrator architectures

# Integrator-Based Filter Design



Any of these different types of integrators can be used to build integrator-based filters

# Are new integrators still being invented?

← → ↻ 🏠 Not secure | patft.uspto.gov/netahtml/PTO/search-bool.html  
📱 Apps 🇻🇪 Yahoo 🇸🇪 Randy Geiger Home 🇺🇸 CNN - Breaking News 🇯🇵 Amazon.com: Online 📄 Google 🇺🇸 My eBay Summary 🇺🇸 GIXEN - Free eBay Au... 📄 Vermont Wooden To... 🇺🇸 Google Scholar 🇺🇸 The Official PVC Wor... 🇺🇸 Home : Panopto 🇺🇸 Vimeo

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Term 1:  in Field 1:

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Results of Search in US Patent Collection db for:

**TTL/integrator** - 531 patents.

Hits 1 through **50 out of 531**

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Oct 16 2018

PAT. NO.	Title
1 10,082,922	<a href="#">Increasing the dynamic range of an integrator based mutual-capacitance measurement circuit</a>
2 10,074,004	<a href="#">Capacitive fingerprint sensor with integrator</a>
3 10,070,089	<a href="#">Inverting amplifier, integrator, sample hold circuit, ad converter, image sensor, and imaging apparatus</a>
4 9,985,594	<a href="#">Gated CDS integrator</a>
5 9,972,003	<a href="#">Pregame electronic commerce integrator</a>
6 9,954,514	<a href="#">Output range for interpolation architectures employing a cascaded integrator-comb (CIC) filter with a multiplier</a>
7 9,885,959	<a href="#">Illumination optical apparatus having deflecting member, lens, polarization member to set polarization in circumference direction, and optical integrator</a>
8 9,885,872	<a href="#">Illumination optical apparatus, exposure apparatus, and exposure method with optical integrator and polarization member that changes polarization state of light</a>
9 9,866,237	<a href="#">Low power switched capacitor integrator, analog-to-digital converter and switched capacitor amplifier</a>
10 9,852,283	<a href="#">Confirming the identity of integrator applications</a>
Nov 2017 11 9,825,646	<a href="#">Integrator and A/D converter using the same</a>
12 9,817,917	<a href="#">System integrator and method for mapping dynamic COBOL constructs to object instances for the automatic integration to object-oriented computing systems</a>
13 9,806,552	<a href="#">Analog/digital converter with charge rebalanced integrator</a>
14 9,800,256	<a href="#">Semiconductor device including integrator and successive approximation register analog-to-digital converter and driving method of the same</a>
15 9,753,559	<a href="#">Feedback integrator current source, transistor, and resistor coupled to input</a>
16 9,726,521	<a href="#">Signal processing apparatus for processing time variant signal with first and second input signals comprising a weighting integrator, a magnitude detector and a gain-adjustable amplifier</a>
17 9,709,242	<a href="#">Shell integrator</a>
18 9,703,178	<a href="#">Projector having a rod integrator with an entrance plane smaller than an area light source</a>
19 9,680,496	<a href="#">Apparatus for overload recovery of an integrator in a sigma-delta modulator</a>
20 9,671,916	<a href="#">Increasing the dynamic range of an integrator based mutual-capacitance measurement circuit</a>
21 9,647,677	<a href="#">Integrator, AD converter, and radiation detection device</a>
22 9,634,688	<a href="#">Integrator, delta-sigma modulator, and communications device</a>
23 9,628,103	<a href="#">Multi-mode discrete-time delta-sigma modulator power optimization using split-integrator scheme</a>
24 9,608,598	<a href="#">Cascaded integrator-comb filter as a non-integer sample rate converter</a>
25 9,588,147	<a href="#">Electronic integrator for Rogowski coil sensors</a>
26 9,574,735	<a href="#">Shell integrator</a>
27 9,558,845	<a href="#">Sampling network and clocking scheme for a switched-capacitor integrator</a>
28 9,531,718	<a href="#">Confirming the identity of integrator applications</a>
29 9,524,054	<a href="#">Integrator and touch sensing system using the same</a>
30 9,519,462	<a href="#">System integrator and method for mapping dynamic COBOL constructs to object instances for the automatic integration to object-oriented computing systems</a>
Nov 2016 31 9,496,969	<a href="#">Double integrator pulse wave shaper apparatus, system and method</a>
32 9,495,563	<a href="#">Analog integrator system and method</a>
33 9,473,075	<a href="#">Dynamic current source for amplifier integrator stages</a>
34 9,467,153	<a href="#">Low power and compact area digital integrator for a digital phase detector</a>
35 9,461,595	<a href="#">Integrator for class D audio amplifier</a>
36 9,454,069	<a href="#">Illumination system having first and second lens arrays including plano-convex lenses wherein some lenses in the second array include a first and a second lens element, projection-type display apparatus, and optical integrator</a>
37 9,405,800	<a href="#">Apparatuses, methods and systems for a universal payment integrator</a>
38 9,389,625	<a href="#">DC-DC converter controller apparatus with dual-counter digital integrator</a>
39 9,383,395	<a href="#">Charge balancing converter using a passive integrator circuit</a>
40 9,379,732	<a href="#">Delta-sigma modulator with reduced integrator requirements</a>
41 9,362,890	<a href="#">Compensation filter for cascaded-integrator-comb decimator</a>
42 9,354,953	<a href="#">System integrator and system integration method with reliability optimized integrated circuit chip selection</a>
43 9,314,389	<a href="#">Therapeutic integrator apparatus</a>
44 9,310,924	<a href="#">Increasing the dynamic range of an integrator based mutual-capacitance measurement circuit</a>
45 9,268,441	<a href="#">Active integrator for a capacitive sense array</a>
46 9,225,351	<a href="#">Current amplifier circuit, integrator, and ad converter</a>
47 9,218,514	<a href="#">Apparatuses and method of switched-capacitor integrator</a>
48 9,171,189	<a href="#">Systems and methods for preventing saturation of analog integrator output</a>
49 9,152,387	<a href="#">System integrator and method for mapping dynamic COBOL constructs to object instances for the automatic integration to object-oriented computing systems</a>
50 9,139,096	<a href="#">One-sided detection and disabling of integrator wind up for speed control in a vehicle</a>



51 [9,063,789](#)  [Hybrid cloud integrator plug-in components](#)

52 [9,061,592](#)  [System and method for detecting power integrator malfunction](#)

53 [9,054,731](#)  [Integrator output swing reduction](#)

54 [9,039,190](#)  [Projector having integrator with greater illuminance in offset direction of projection lens and modulator](#)

55 [9,037,469](#)  [Automated communication integrator](#)

56 [9,014,322](#)  [Low power and compact area digital integrator for a digital phase detector](#)

57 [9,009,697](#)  [Hybrid cloud integrator](#)

58 [8,995,061](#)  [Speckle reduction using lenslet integrator](#)

59 [8,988,904](#)  [Power supply with integrator for controlling current](#)

60 [8,957,363](#)  [Differential photodiode integrator circuit for absorbance measurements](#)

61 [8,952,749](#)  [Filter with combined resonator and integrator](#)

62 [8,941,526](#)  [Time integrator and DELTA, SIGMA, time-to-digital converter](#)

63 [8,937,567](#)  [Delta-sigma modulator, integrator, and wireless communication device](#)

64 [8,922,290](#)  [Pulse width modulator with two-way integrator](#)

65 [8,866,532](#)  [Passive integrator and method](#)

66 [8,866,531](#)  [Broadband analog radio-frequency integrator](#)

67 [8,860,491](#)  [Integrator output swing reduction technique for sigma-delta analog-to-digital converters](#)

68 [8,854,107](#)  [Integrator circuit with inverting integrator and non-inverting integrator](#)

69 [8,851,684](#)  [Optical unit including an integrator optical system, and projection display device including the optical unit](#)

70 [8,835,827](#)  [Current integrator with wide dynamic range](#)

71 [8,824,626](#)  [Reduced-noise integrator, detector and CT circuits](#)

72 [8,816,763](#)  [Integrator input error correction circuit and circuit method](#)

73 [8,779,831](#)  [Integrator](#)

74 [8,775,003](#)  [Methods and systems for controlling a proportional integrator](#)

75 [8,767,343](#)  [Disk drive increasing integrator output range to complete seek operation](#)

76 [8,724,080](#)  [Optical raster element, optical integrator and illumination system of a microlithographic projection exposure apparatus](#)

77 [8,704,580](#)  [Circuit sharing time delay integrator](#)

78 [8,674,864](#)  [Integrator and oversampling A/D converter having the same](#)

79 [8,665,129](#)  [Complex second-order integrator and oversampling A/D converter having the same](#)

80 [8,659,343](#)  [Calibration for mixed-signal integrator architecture](#)

81 [8,653,867](#)  [Pulse modulated neural integrator circuit and associated phase locked loop](#)

82 [8,639,513](#)  [Automated communication integrator](#)

83 [8,638,420](#)  [Optical integrator, illuminating optical device, exposure apparatus and device manufacturing method](#)

84 [8,614,639](#)  [Integrator ramp generator with DAC and switched capacitors](#)

85 [8,611,013](#)  [Optical integrator, illumination optical device, aligner, and method for fabricating device](#)

86 [8,587,764](#)  [Optical integrator system, illumination optical apparatus, exposure apparatus, and device manufacturing method](#)

87 [8,575,988](#)  [Mixed-signal integrator architecture](#)

88 [8,573,779](#)  [Lighting device with plural light sources illuminating distinct regions of integrator](#)

89 [8,566,277](#)  [System integrator and method for mapping dynamic COBOL constructs to object instances for the automatic integration to object-oriented computing systems](#)

90 [8,564,358](#)  [Integrator circuit with multiple time window functions](#)

91 [8,558,610](#)  [Integrator input error correction circuit and circuit method](#)

92 [8,536,923](#)  [Integrator distortion correction circuit](#)

93 [8,526,487](#)  [Differential energy difference integrator](#)

94 [8,520,307](#)  [Optical integrator for an illumination system of a microlithographic projection exposure apparatus](#)

95 [8,504,503](#)  [Pulse modulated neural integrator circuit](#)

96 [8,497,977](#)  [Optical integrator, illumination optical system, exposure apparatus, and device manufacturing method](#)

97 [8,438,201](#)  [Digital fractional integrator](#)

98 [8,432,150](#)  [Methods for operating an array column integrator](#)

99 [8,432,149](#)  [Array column integrator](#)


























100 [8,422,018](#)  [Optical measurement apparatus including hemispherical optical integrator](#)

July 2014

Oct 16 2012


























PAT. NO.	Title
1 <a href="#">8,290,897</a>	<a href="#">System integrator and method for mapping dynamic COBOL constructs to object instances for the automatic integration to object-oriented computing systems</a>
2 <a href="#">8,283,966</a>	<a href="#">Integrator circuit</a>
3 <a href="#">8,275,307</a>	<a href="#">Vehicle audio integrator</a>
4 <a href="#">8,264,388</a>	<a href="#">Frequency integrator with digital phase error message for phase-locked loop applications</a>
5 <a href="#">8,258,990</a>	<a href="#">Integrator, resonator, and oversampling A/D converter</a>
6 <a href="#">8,253,473</a>	<a href="#">Integrated circuit of an integrator with enhanced stability and related stabilization method</a>
7 <a href="#">8,199,038</a>	<a href="#">Active resistance-capacitor integrator and continuous-time sigma-delta modulator with gain control function</a>
8 <a href="#">8,164,873</a>	<a href="#">Integrator and circuit-breaker having an integrator</a>
9 <a href="#">8,145,597</a>	<a href="#">System integrator and method for mapping dynamic COBOL constructs to object instances for the automatic integration to object-oriented computing systems</a>
10 <a href="#">8,129,972</a>	<a href="#">Single integrator sensorless current mode control for a switching power converter</a>
11 <a href="#">8,125,262</a>	<a href="#">Low power and low noise switched capacitor integrator with flexible input common mode range</a>
12 <a href="#">8,098,377</a>	<a href="#">Electric gated integrator detection method and device thereof</a>
13 <a href="#">8,081,098</a>	<a href="#">Integrator, delta-sigma modulator, analog-to-digital converter and applications thereof</a>
14 <a href="#">8,035,439</a>	<a href="#">Multi-channel integrator</a>
15 <a href="#">8,031,404</a>	<a href="#">Fly's eye integrator, illuminator, lithographic apparatus and method</a>
16 <a href="#">8,029,144</a>	<a href="#">Color mixing rod integrator in a laser-based projector</a>
17 <a href="#">8,028,304</a>	<a href="#">Component integrator</a>
18 <a href="#">8,013,657</a>	<a href="#">Temperature compensated integrator</a>
19 <a href="#">8,011,810</a>	<a href="#">Light integrator for more than one lamp</a>
20 <a href="#">7,997,740</a>	<a href="#">Integrator unit</a>
21 <a href="#">7,965,795</a>	<a href="#">Prevention of integrator wind-up in PI type controllers</a>
22 <a href="#">7,965,151</a>	<a href="#">Pulse width modulator with two-way integrator</a>
23 <a href="#">7,954,962</a>	<a href="#">Laser image display, and optical integrator and laser light source package used in such laser image display</a>
24 <a href="#">7,943,893</a>	<a href="#">Illumination optical system and image projection device having a rod integrator uniformizing spatial energy distribution of diffused illumination beam</a>
25 <a href="#">7,933,812</a>	<a href="#">System integrator and commodity roll-up</a>

Apr 26 2011

- 26 [7,932,960](#)  [Integrator array for HUD backlighting](#)
- 27 [7,911,256](#)  [Dual integrator circuit for analog front end \(AFE\)](#)
- 28 [7,907,115](#)  [Digitally synchronized integrator for noise rejection in system using PWM dimming signals to control brightness of cold cathode fluorescent lamp for backlighting liquid crystal display](#)
- 29 [7,905,631](#)  [Illumination system having coherent light source and integrator rotatable transverse the illumination axis](#)
- 30 [7,884,662](#)  [Multi-channel integrator](#)
- 31 [7,880,969](#)  [Optical integrator for an illumination system of a microlithographic projection exposure apparatus](#)
- 32 [7,873,223](#)  [Cognition integrator and language](#)
- 33 [7,834,963](#)  [Optical integrator](#)
- 34 [7,830,197](#)  [Adjustable integrator using a single capacitance](#)
- 35 [RE41,792](#)  [Controllable integrator](#)
- 36 [7,788,309](#)  [Interleaved comb and integrator filter structures](#)
- 37 [7,773,730](#)  [Voice record integrator](#)
- 38 [7,729,577](#)  [Waveguide-optical Kohler integrator utilizing geodesic lenses](#)
- 39 [7,726,819](#)  [Structure for protecting a rod integrator having a light shield plate with an opening](#)
- 40 [7,724,063](#)  [Integrator-based common-mode stabilization technique for pseudo-differential switched-capacitor circuits](#)
- 41 [7,714,634](#)  [Pseudo-differential active RC integrator](#)
- 42 [7,706,072](#)  [Optical integrator, illumination optical device, photolithograph, photolithography, and method for fabricating device](#)
- 43 [7,696,913](#)  [Signal processing system using delta-sigma modulation having an internal stabilizer path with direct output-to-integrator connection](#)
- 44 [7,693,430](#)  [Burst optical receiver with AC coupling and integrator feedback network](#)
- 45 [7,679,540](#)  [Double sampling DAC and integrator](#)
- 46 [7,671,774](#)  [Analog-to-digital converter with integrator circuit for overload recovery](#)
- 47 [7,658,497](#)  [Rod integrator holder and projection type video display](#)
- 48 [7,629,917](#)  [Integrator and cyclic AD converter using the same](#)
- 49 [7,619,550](#)  [Delta-sigma AD converter apparatus using delta-sigma modulator circuit provided with reset circuit resetting integrator](#)
- 50 [7,611,246](#)  [Projection display and optical integrator](#)

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Nov 3 2009

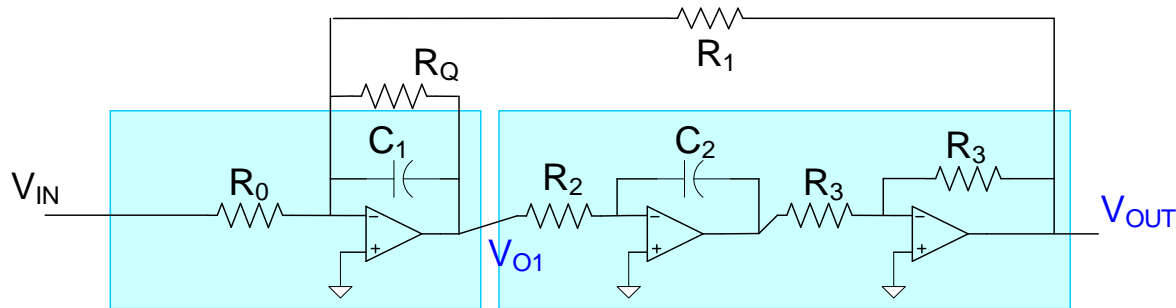
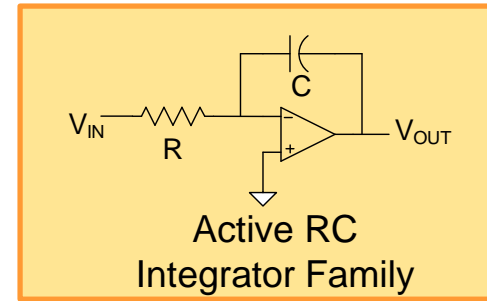
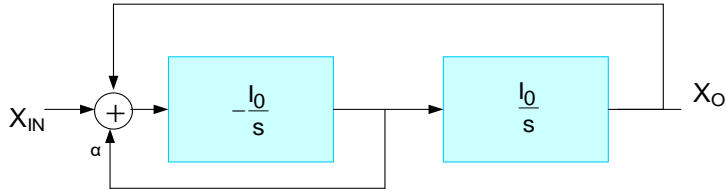
PAT. NO.	Title
51	<a href="#">7.605.645</a>  <a href="#">Transconductor, integrator, and filter circuit</a>
52	<a href="#">7.599.631</a>  <a href="#">Burst optical receiver with AC coupling and integrator feedback network</a>
53	<a href="#">7.575.159</a>  <a href="#">Point of sale integrator</a>
54	<a href="#">7.570.032</a>  <a href="#">Regulator with integrator in feedback signal</a>
55	<a href="#">7.565.326</a>  <a href="#">Dialect independent multi-dimensional integrator using a normalized language platform and secure controlled access</a>
56	<a href="#">7.554.400</a>  <a href="#">Integrator and error amplifier</a>
57	<a href="#">7.543.945</a>  <a href="#">Integrator module with a collimator and a compact light source and projection display having the same</a>
58	<a href="#">7.532.145</a>  <a href="#">High resolution and wide dynamic range integrator</a>
59	<a href="#">7.528.818</a>  <a href="#">Digitally synchronized integrator for noise rejection in system using PWM dimming signals to control brightness of light source</a>
60	<a href="#">7.511.648</a>  <a href="#">Integrating/SAR ADC and method with low integrator swing and low complexity</a>
61	<a href="#">7.474.241</a>  <a href="#">Delta-sigma modulator provided with a charge sharing integrator</a>
62	<a href="#">7.471.456</a>  <a href="#">Optical integrator, illumination optical device, exposure device, and exposure method</a>
63	<a href="#">7.454.750</a>  <a href="#">Integrator adaptor and proxy based composite application provisioning method and apparatus</a>
64	<a href="#">7.447.049</a>  <a href="#">Single ended flyback power supply controllers with integrator to integrate the difference between feedback signal a reference signal</a>
65	<a href="#">7.423.729</a>  <a href="#">Method of monitoring the light integrator of a photolithography system</a>
66	<a href="#">7.417.485</a>  <a href="#">Differential energy difference integrator</a>
67	<a href="#">7.415.716</a>  <a href="#">Component integrator</a>
68	<a href="#">7.411.534</a>  <a href="#">Analog-to-digital converter (ADC) having integrator dither injection and quantizer output compensation</a>
69	<a href="#">7.411.198</a>  <a href="#">Integrator circuitry for single channel radiation detector</a>
70	<a href="#">7.395.090</a>  <a href="#">Personal portable integrator for music player and mobile phone</a>
71	<a href="#">7.385.426</a>  <a href="#">Low current offset integrator with signal independent low input capacitance buffer circuit</a>
72	<a href="#">7.379.160</a>  <a href="#">Optical integrator, illumination optical device, exposure apparatus, and exposure method</a>
73	<a href="#">7.352.510</a>  <a href="#">Light-pipe integrator for uniform irradiance and intensity</a>
74	<a href="#">7.345.285</a>  <a href="#">Spectra acquisition system with threshold adaptation integrator</a>
75	<a href="#">7.333.626</a>  <a href="#">Arbitrary coverage angle sound integrator</a>
..	..

- 76 [7,324,654](#) **T** [Arbitrary coverage angle sound integrator](#)
- 77 [7,324,025](#) **T** [Non-integer interpolation using cascaded integrator-comb filter](#)
- 78 [7,315,268](#) **T** [Integrator current matching](#)
- 79 [7,304,592](#) **T** [Method of adding a dither signal in output to the last integrator of a sigma-delta converter and relative sigma-delta converter](#)
- 80 [7,280,405](#) **T** [Integrator-based current sensing circuit for reading memory cells](#)
- 81 [7,262,056](#) **T** [Enhancing intermolecular integration of nucleic acids using integrator complexes](#)
- 82 [7,243,844](#) **T** [Point of sale integrator](#)
- 83 [7,242,333](#) **T** [Alternate sampling integrator](#)
- 84 [7,205,849](#) **T** [Phase locked loop including an integrator-free loop filter](#)
- 85 [7,187,948](#) **T** [Personal portable integrator for music player and mobile phone](#)
- 86 [7,182,468](#) **T** [Dual lamp illumination system using multiple integrator rods](#)
- 87 [7,180,357](#) **T** [Operational amplifier integrator](#)
- 88 [7,170,959](#) **T** [Tailored response cascaded integrator comb digital filter and methodology for parallel integrator processing](#)
- 89 [7,155,470](#) **T** [Variable gain integrator](#)
- 90 [7,152,981](#) **T** [Projection illumination system with tunnel integrator and field lens](#)
- 91 [7,152,084](#) **T** [Parallelized infinite impulse response \(IIR\) and integrator filters](#)
- 92 [7,150,968](#) **T** [Bridging INtegrator-2 \(Bin2\) nucleic acid molecules and proteins and uses therefor](#)
- 93 [7,138,848](#) **T** [Switched capacitor integrator system](#)
- 94 [7,130,764](#) **T** [Robust DSP integrator for accelerometer signals](#)
- 95 [7,102,844](#) **T** [Dual direction integrator for constant velocity control for an actuator using sampled back EMF control](#)
- 96 [7,102,548](#) **T** [Cascaded integrator comb filter with arbitrary integer decimation value and scaling for unity gain](#)
- 97 [7,098,845](#) **T** [Apparatus for generating an integrator timing reference from a local oscillator signal](#)
- 98 [7,098,827](#) **T** [Integrator circuit](#)
- 99 [7,098,718](#) **T** [Tunable current-mode integrator for low-frequency filters](#)
- 100 [7,087,881](#) **T** [Solid state image pickup device including an integrator with a variable reference potential](#)

PAT. NO.	Title
501 4,161,658	Wind turbine generator having integrator tracking
502 4,160,954	Multiple rate discharge circuit for integrator, especially for use in computerized axial tomography
503 4,154,102	Continuous integrator control linkage
504 4,140,062	Differential integrator
505 4,132,923	Circuit for light-integrator-controlled electronic flash unit
506 4,122,528	Integrator circuits for a constant velocity vector generator
507 4,083,365	Dual integrator EEG analyzer
508 4,081,733	Automatic control system with integrator offset
509 4,061,033	Temperature function integrator
510 4,059,751	Logic controlled integrator
511 4,053,746	System and method for operating a steam turbine with digital computer control having integrator limit
512 4,042,842	Multiple-time-constant integrator or differentiator
513 4,035,809	Electronic integrator for chart recorder
514 4,030,038	Multiple dumping integrator
515 4,023,019	Automatic scaled digital integrator
516 4,012,730	Doppler detection device with integrator sampling means to inhibit false alarms
517 4,006,415	Fast reset integrator
518 4,006,317	Electrostatic transducer and acoustic and electric signal integrator
519 4,002,067	Low friction absolute pressure continuous integrator
520 4,001,721	Field effect transistor Miller integrator oscillator with temperature compensating impedance
521 3,991,730	Noise immune reset circuit for resetting the integrator of an electronic engine spark timing controller
522 3,990,073	Digital signal processing arrangement using a cascaded integrator function generator
523 3,989,961	Bidirectional reset integrator converter
524 3,980,865	Electronic integrator for gas volume calculations
525 3,975,682	Watt/watthour transducer and integrator and current sources therefor
526 3,971,993	High capacity recirculating delay loop integrator
527 3,961,173	Heat unit integrator for X-ray tubes
528 3,946,609	Barometrically compensated pressure index continuous integrator for measuring throughput fluid flow of meters
529 3,943,456	Signal generator for electronic musical instrument, employing variable rate integrator
530 3,942,131	Low frequency two phase oscillator including variable feedback integrator circuits
531 3,931,619	Overtemperature monitor and integrator apparatus

Jan 1976

# Example – Active RC Feedback Tow Thomas Biquad



$$\left. \begin{aligned} V_{OUT} &= \frac{1}{sR_2C_2} V_{O1} \\ V_{IN}G_0 + V_{O1}(sC_1 + G_Q) + G_1V_{OUT} &= 0 \end{aligned} \right\}$$

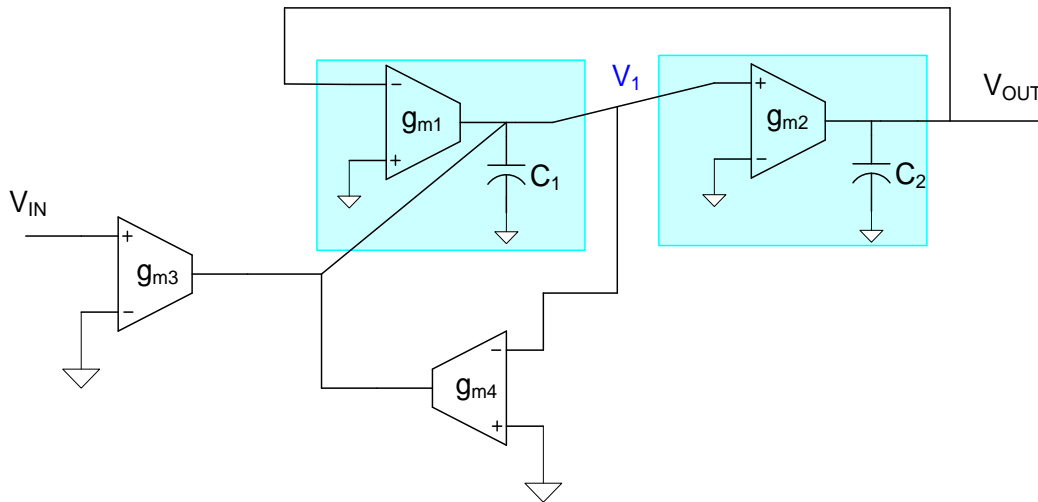
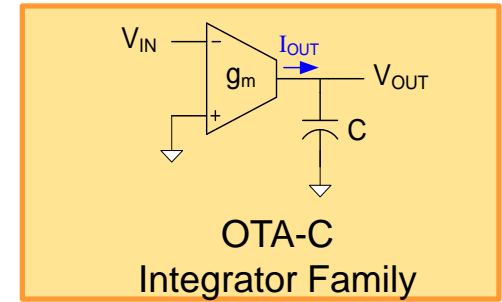
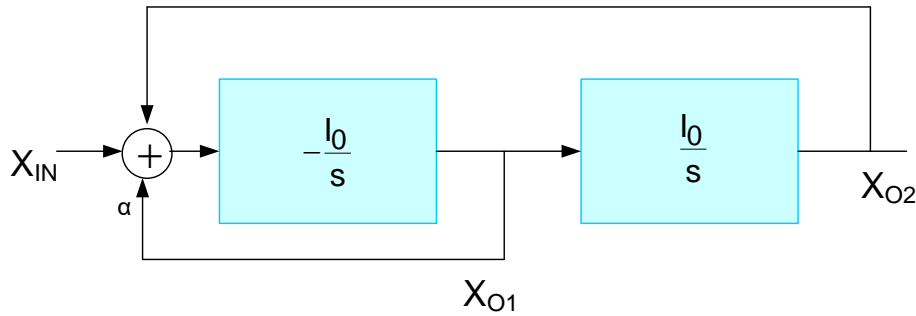
$$\frac{V_{OUT}}{V_{IN}} = - \frac{1}{s^2 + s \frac{1}{R_Q C_1} + \frac{1}{C_1 R_1 R_2 C_2}}$$

If  $R_1 = R_2 = R$  and  $C_1 = C_2 = C$

$$\omega_0 = \frac{1}{RC}$$

$$Q = \frac{R_Q}{R}$$

# Example – OTA-C Tow Thomas Biquad



$$\frac{V_{OUT}}{V_{IN}} = \frac{g_{m3}g_{m2}}{(s^2C_1C_2 + sg_{m4}C_2 + g_{m1}g_{m2})}$$

Assume  $g_{m1}=g_{m2}=g_m$ ,  $C_1=C_2=C$

$$\frac{V_{OUT}}{V_{IN}} = \frac{\left(\frac{g_{m3}}{g_m}\right) \frac{g_m^2}{C^2}}{\left(s^2 + s\left(\frac{g_{m4}}{g_m}\right) \frac{g_m}{C} + \frac{g_m^2}{C^2}\right)}$$

express as

$$\frac{V_{OUT}}{V_{IN}} = \frac{\left(\frac{g_{m3}}{g_m}\right) \omega_0^2}{\left(s^2 + s\frac{\omega_0}{Q} + \omega_0^2\right)}$$

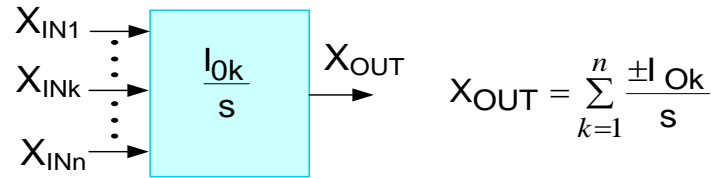
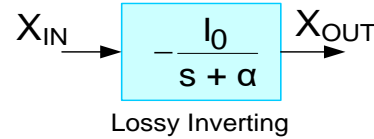
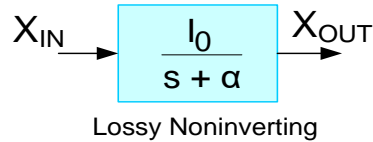
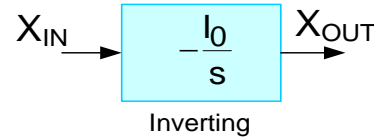
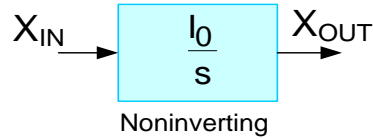
where

$$\omega_0 = \frac{g_m}{C} \quad Q = \frac{g_m}{g_{m4}}$$

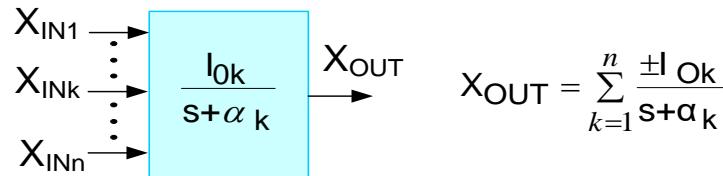
$$\left. \begin{aligned} V_{OUT} sC_2 &= g_{m2} V_1 \\ V_1 sC_1 &= -g_{m1} V_{OUT} + g_{m3} V_{IN} - g_{m4} V_1 \end{aligned} \right\}$$



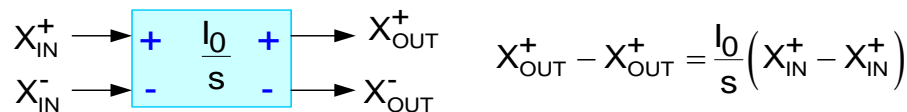
# Basic Integrator Functionality (for all families)



Summing (Multiple-Input) Inverting/Noninverting



Summing (Multiple-Input) Lossy Inverting/Noninverting

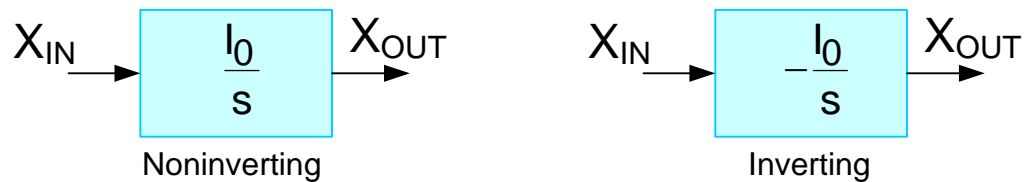


Balanced Differential



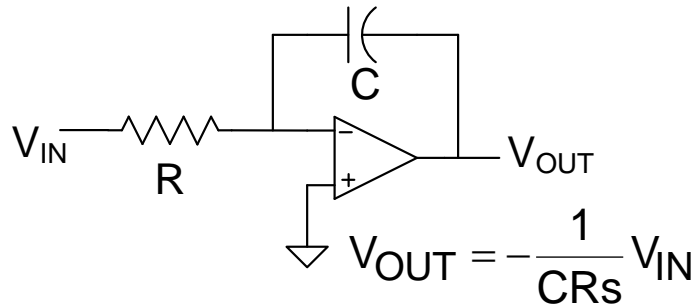
Fully Differential

# Basic Integrator Functionality

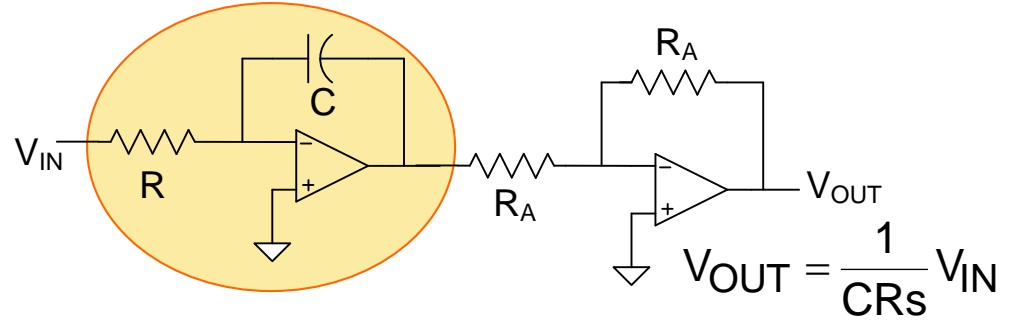


- An inverting/noninverting integrator pair define a family of integrators
- All integrator functional types can usually be obtained from the inverting/noninverting integrator pair
- Suffices to focus primarily on the design of the inverting/noninverting integrator pair since properties of class primarily determined by properties of integrator pair

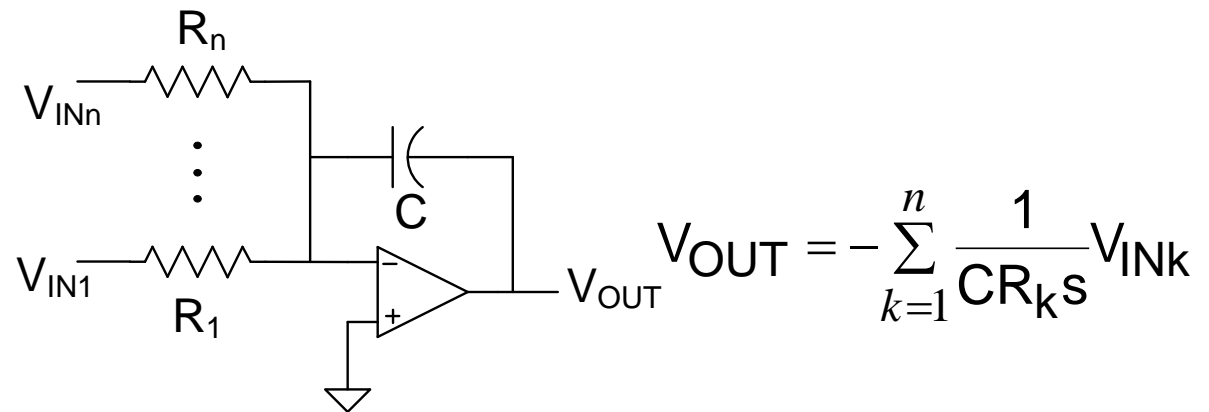
# Example – Basic Op-Amp Feedback Integrator Family



Inverting Integrator of Family

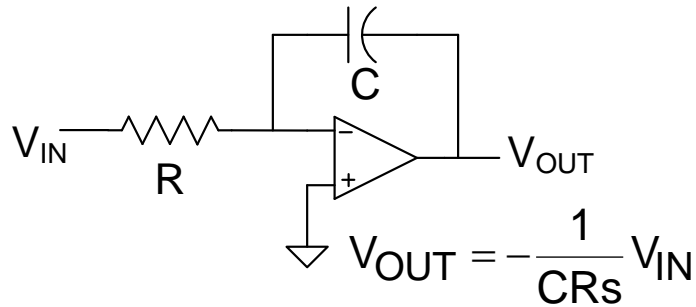


Noninverting Integrator



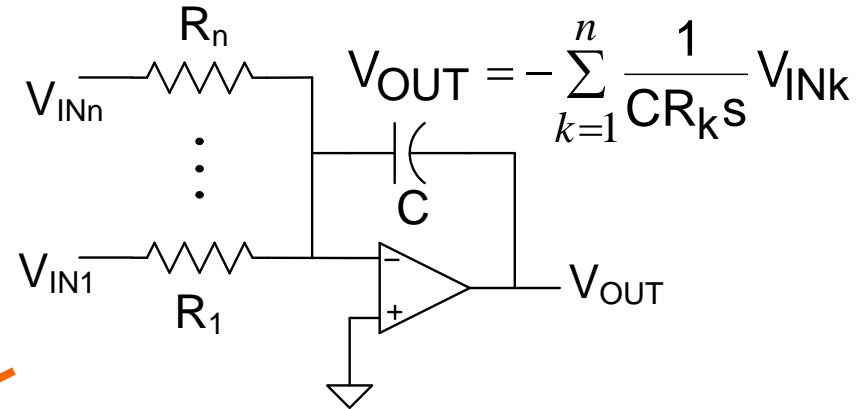
Summing Inverting Integrator

# Example – Basic Op-Amp Feedback Integrator Family



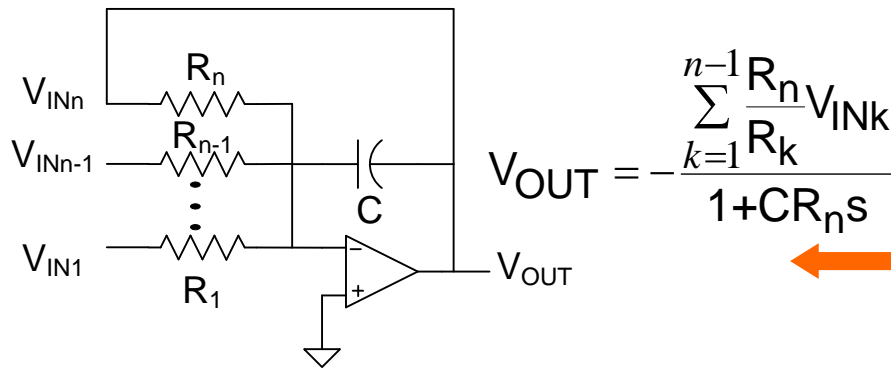
$$V_{OUT} = -\frac{1}{CRs} V_{IN}$$

Inverting Integrator of Family



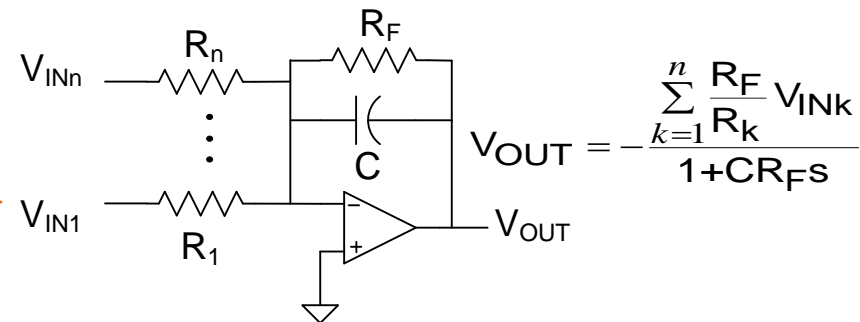
$$V_{OUT} = -\sum_{k=1}^n \frac{1}{CR_k s} V_{INk}$$

Summing Inverting Integrator



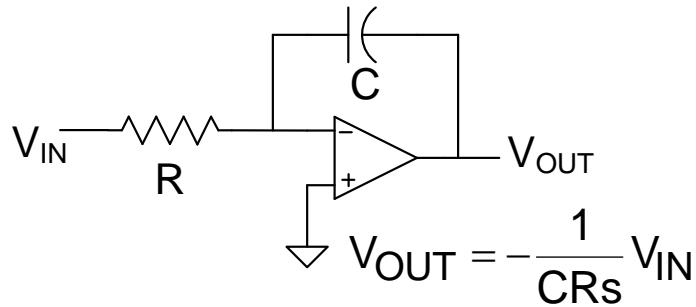
$$V_{OUT} = -\frac{\sum_{k=1}^{n-1} \frac{R_n}{R_k} V_{INk}}{1+CR_n s}$$

Lossy Summing Inverting Integrator

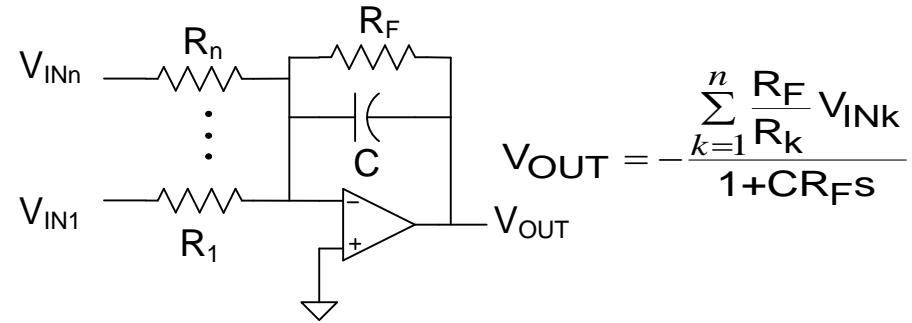


$$V_{OUT} = -\frac{\sum_{k=1}^n \frac{R_F}{R_k} V_{INk}}{1+CR_F s}$$

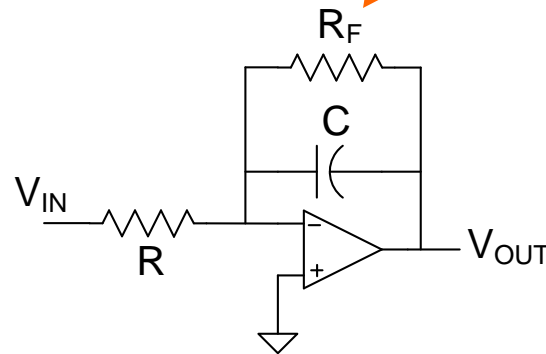
# Example – Basic Op-Amp Feedback Integrator



Inverting Integrator of Family



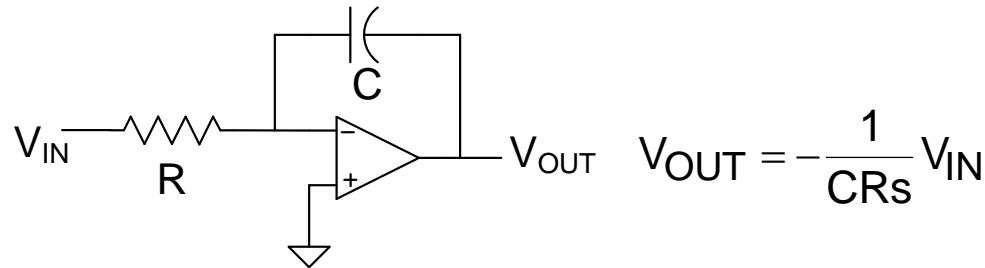
Lossy Summing Inverting Integrator



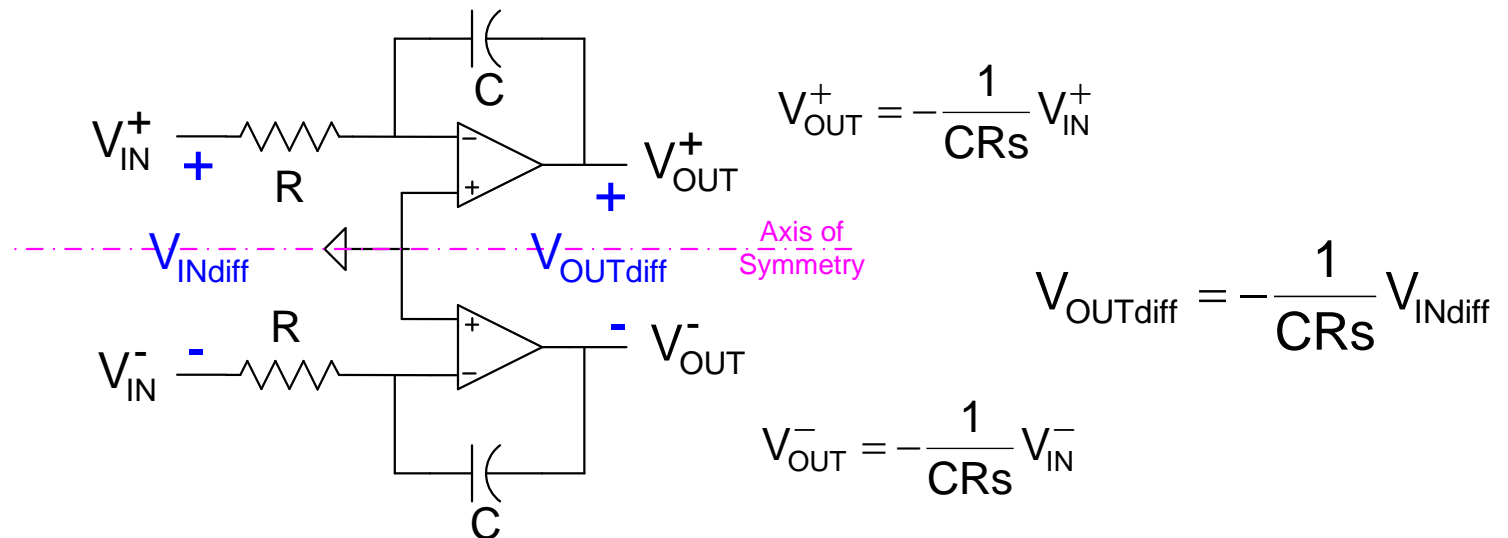
Lossy Inverting Integrator

$$V_{OUT} = -\frac{R_F/R}{1 + CR_Fs} V_{IN}$$

# Example – Basic Op-Amp Feedback Integrator Family

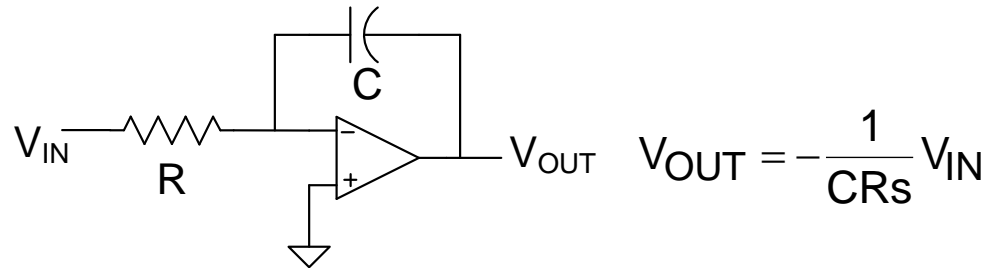


Inverting Integrator of Family

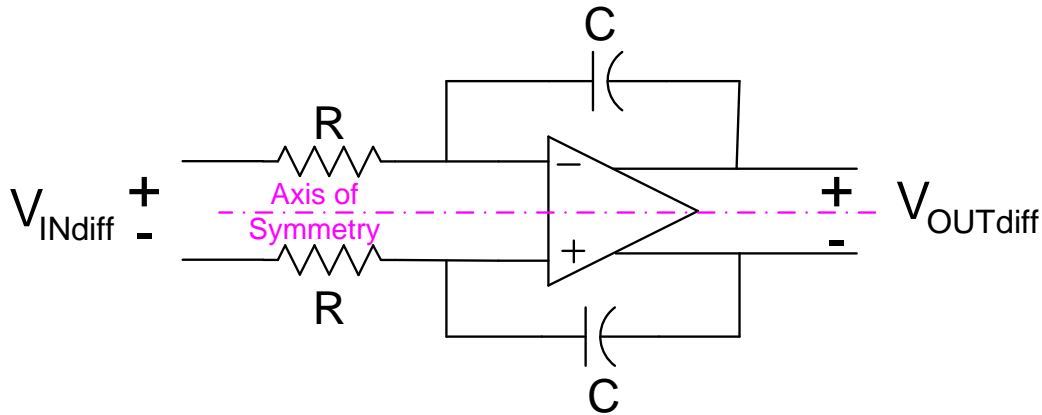


Balanced Differential Inverting Integrator

# Example – Basic Op-Amp Feedback Integrator Family



Inverting Integrator of Family

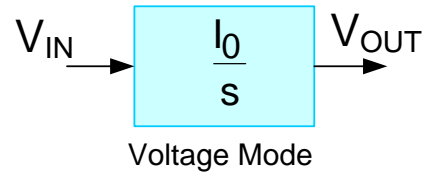


$$V_{OUTdiff} = -\frac{1}{CRs} V_{INdiff}$$

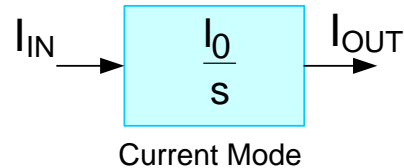
Fully Differential Inverting Integrator

Note distinction between fully balanced and fully differential structures !

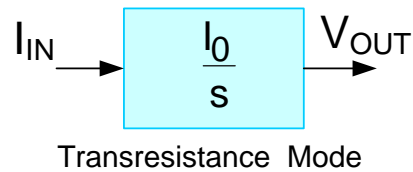
# Integrator Types



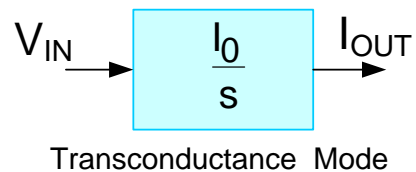
$$V_{OUT} = \frac{I_0}{s} V_{IN}$$



$$I_{OUT} = \frac{I_0}{s} I_{IN}$$



$$V_{OUT} = \frac{I_0}{s} I_{IN}$$



$$I_{OUT} = \frac{I_0}{s} V_{IN}$$

Will consider first the Voltage Mode type of integrators



# Voltage Mode Integrators

→ Active RC (Feedback-based)

- MOSFET-C (Feedback-based)

- OTA-C

- TA-C

} Sometimes termed “current mode”

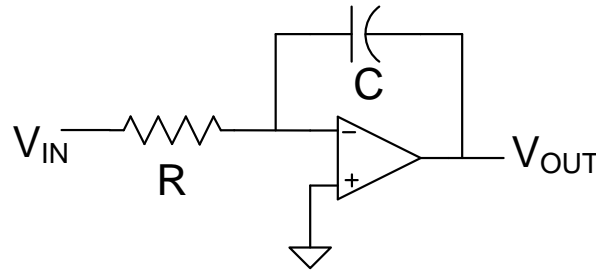
- Other Continuous-time Structures

- Switched Capacitor

- Switched Resistor

} Discrete Time

# Active RC Voltage Mode Integrator



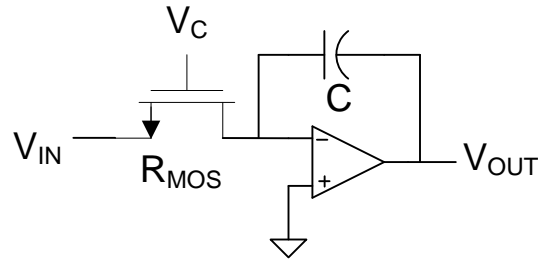
$$V_{OUT} = -\frac{1}{CRs} V_{IN}$$

- Limited to low frequencies because of Op Amp limitations
- No good resistors for monolithic implementations
  - Area for passive resistors is too large at low frequencies
  - Some recent work by Haibo Fei shows promise for some audio frequency applications
- Capacitor area too large at low frequencies for monolithic implementations
- Active devices are highly temperature dependent, proc. dependent, and nonlinear
- No practical tuning or trimming scheme for integrated applications with passive resistors

# Voltage Mode Integrators

- Active RC (Feedback-based)
  - • MOSFET-C (Feedback-based)
  - OTA-C
  - TA-C
- } Sometimes termed “current mode”
- Other Continuous-time Structures
  - Switched Capacitor
  - Switched Resistor
- } Discrete Time

# MOSFET-C Voltage Mode Integrator

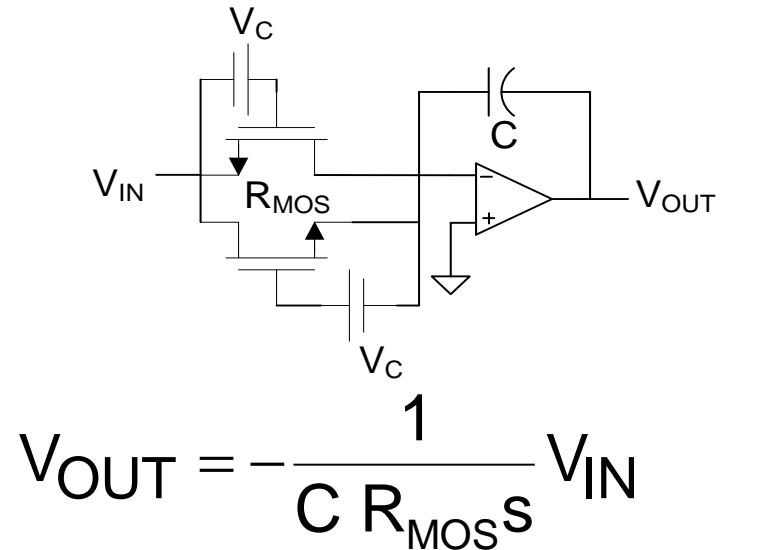
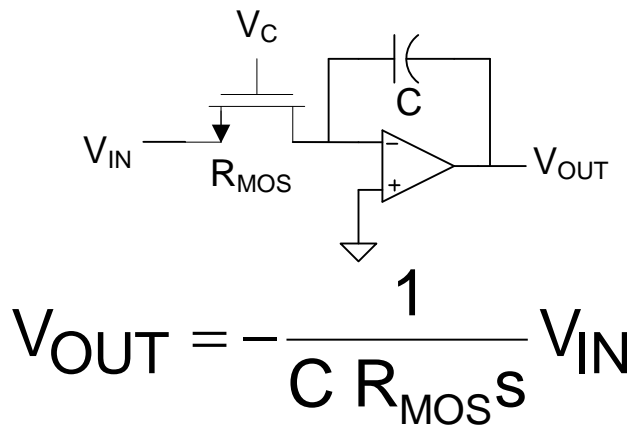


$$V_{OUT} = -\frac{1}{CR_{MOS}S} V_{IN}$$

- Limited to low frequencies because of Op Amp limitations
- Area for  $R_{MOS}$  is manageable !
- Active devices are highly temperature dependent, process dependent
- Potential for tuning with  $V_C$
- Highly Nonlinear (can be partially compensated with cross-coupled input)

**A Solution without a Problem**

# MOSFET-C Voltage Mode Integrator



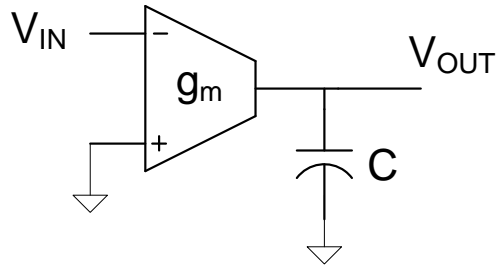
- Improved Linearity
- Some challenges for implementing  $V_C$

Still A Solution without a Problem

# Voltage Mode Integrators

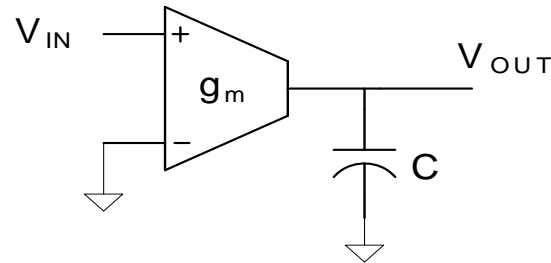
- Active RC (Feedback-based)
  - MOSFET-C (Feedback-based)
  - OTA-C
  - TA-C
- } Sometimes termed “current mode”
- Other Continuous-time Structures
  - Switched Capacitor
  - Switched Resistor
- } Discrete Time

# OTA-C Voltage Mode Integrator



$$V_{OUT} = -\frac{g_m}{sC} V_{IN}$$

Inverting



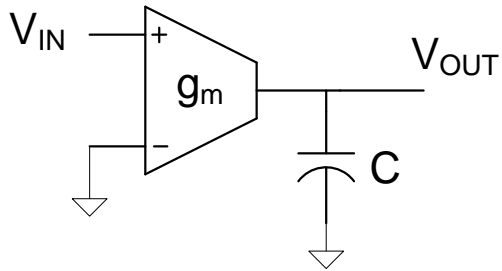
$$V_{OUT} = \frac{g_m}{sC} V_{IN}$$

Noninverting

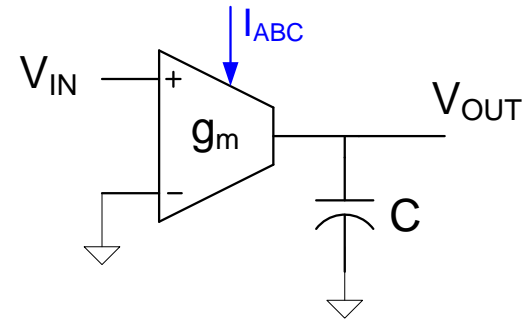
- Requires only two components
- Inverting and Noninverting structures of same complexity
- Good high-frequency performance
- Small area
- Linearity is limited (no feedback in integrator)
- Susceptible to process and temperature variations
- Tuning control can be readily added

Widely used in high frequency applications

# OTA-C Voltage Mode Integrator



$$V_{OUT} = \frac{g_m}{sC} V_{IN}$$



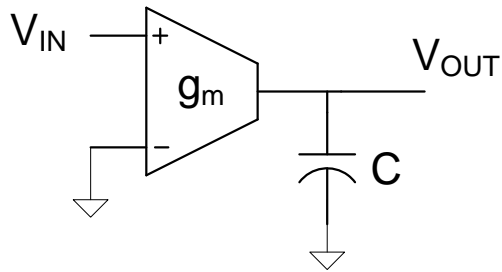
$$V_{OUT} = \frac{g_m}{sC} V_{IN}$$

$$g_m = f(I_{ABC})$$

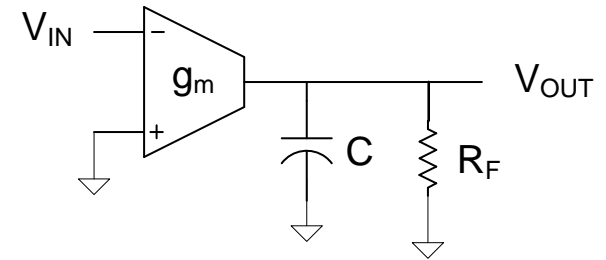
Programmable Integrator



# OTA-C Voltage Mode Integrator



$$V_{OUT} = \frac{g_m}{sC} V_{IN}$$

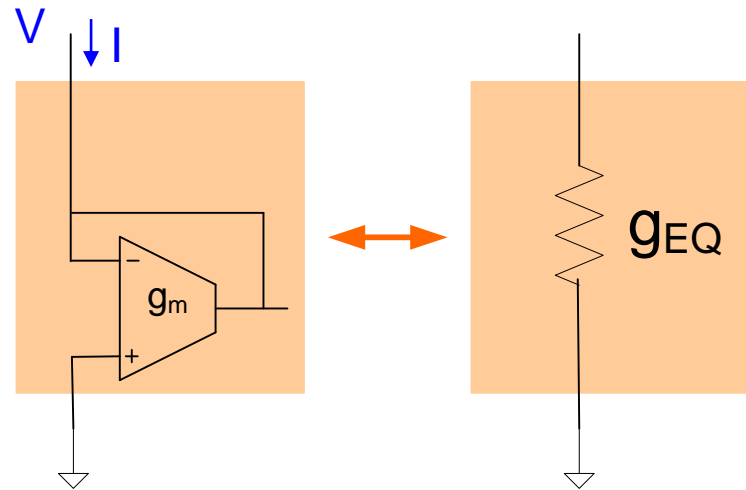


$$\frac{V_{OUT}(s)}{V_{IN}(s)} = \frac{g_m R_F}{1 + s(R_F C)}$$

Lossy Integrator

But  $R_F$  is typically too large for integrated applications

# OTA-C Voltage Mode Integrator



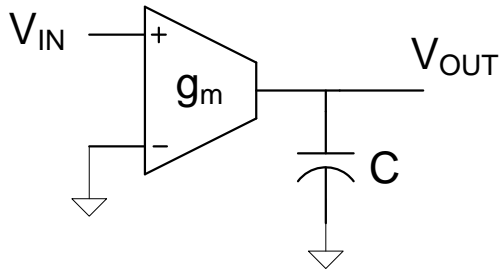
$$I = -g_m V$$

$$g_{EQ} = \frac{I}{V}$$

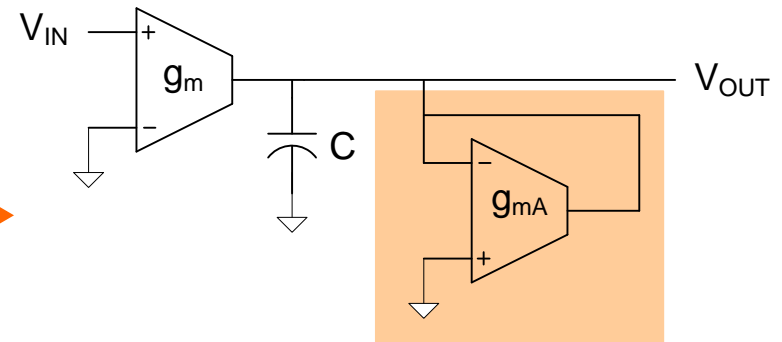
$$g_{EQ} = g_m$$

OTA is generally much smaller than a resistor

# OTA-C Voltage Mode Integrator



$$V_{OUT} = \frac{g_m}{sC} V_{IN}$$

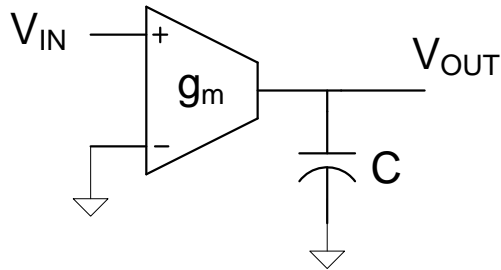


$$\frac{V_{OUT}(s)}{V_{IN}(s)} = \frac{g_m/g_{mA}}{1+s(C/g_{mA})}$$

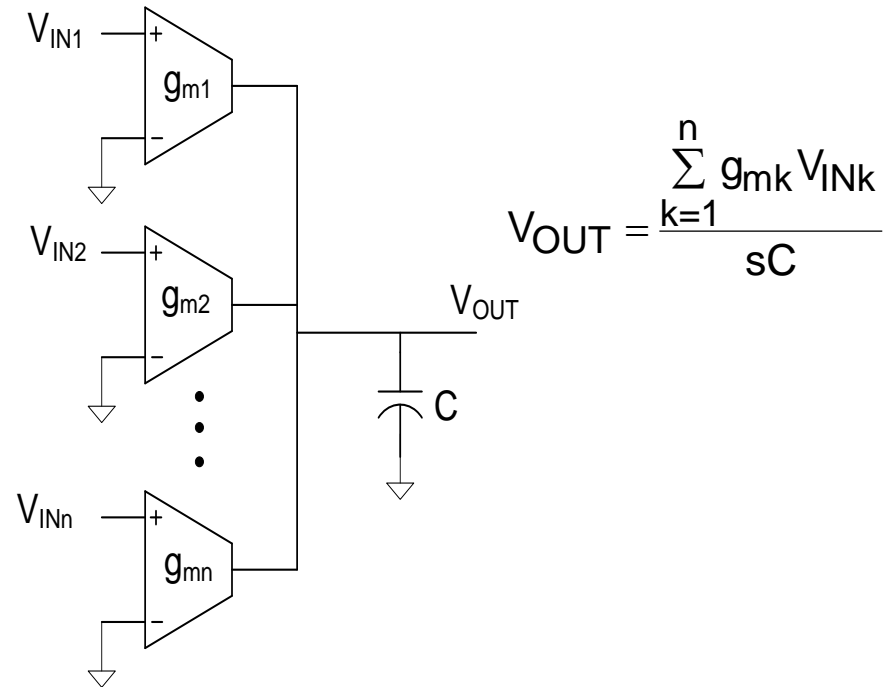
Lossy Integrator

- Practical implementation
- Both OTAs can be readily programmable

# OTA-C Voltage Mode Integrator



$$V_{OUT} = \frac{g_m}{sC} V_{IN}$$

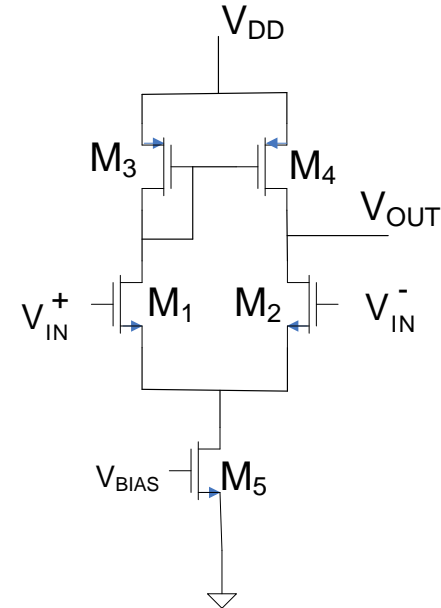
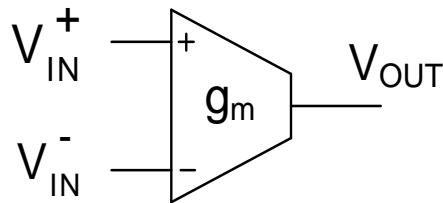


$$V_{OUT} = \frac{\sum_{k=1}^n g_{mk} V_{INk}}{sC}$$

Summing Integrator

- Inverting and noninverting functions can be combined in single summer
- All transconductance gains can be programmable

# OTA Architecture



Mid-complexity OTA

- $M_1$  and  $M_2$  matched
- $M_2$  and  $M_4$  matched
- Define  $M$  to be the gain of the current mirror formed with  $M_2$  and  $M_4$
- $g_m$  programmable with  $V_{BIAS}$

$$g_m = \frac{g_{m1}}{2}(1+M)$$

Often  $M=1$

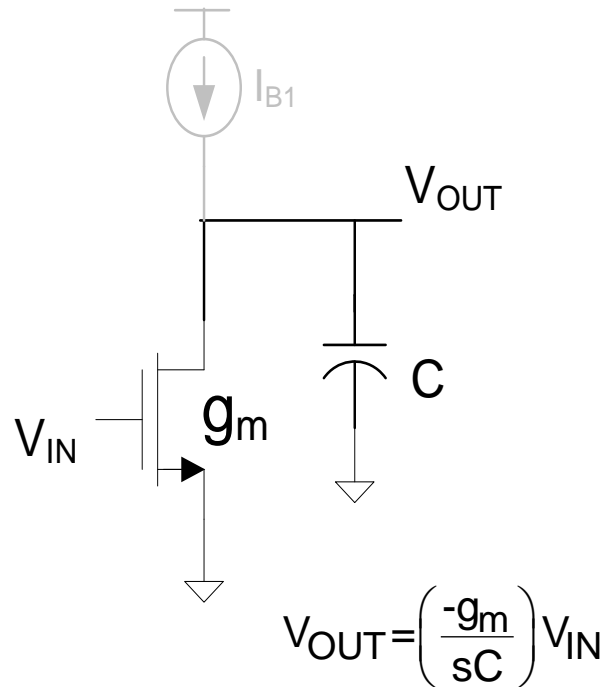
$$g_m = g_{m1}$$

Other OTAs exist, considerable effort expended over past two decades on OTA design

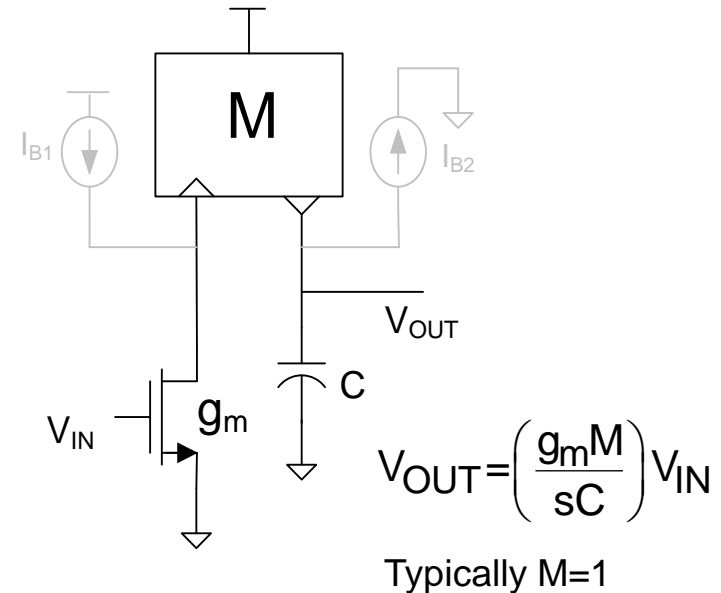
# Voltage Mode Integrators

- Active RC (Feedback-based)
  - MOSFET-C (Feedback-based)
  - OTA-C
  - TA-C
- } Sometimes termed “current mode”
- Other Continuous-time Structures
  - Switched Capacitor
  - Switched Resistor
- } Discrete Time

# TA-C Voltage Mode Integrator



Inverting Integrator

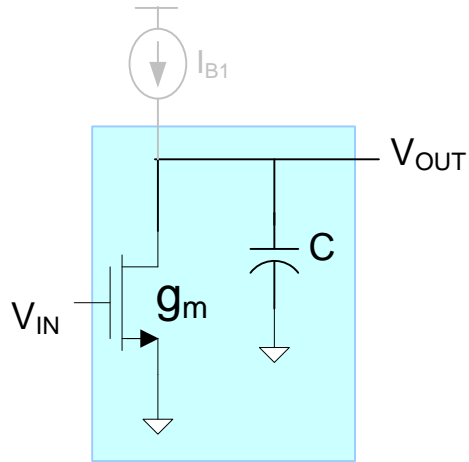


Noninverting Integrator

- Can operate at very high frequencies
- Low device count circuit
- Simplicity is important for operating at very high frequencies
- $I_0$  is process and temperature dependent
- Linearity is limited

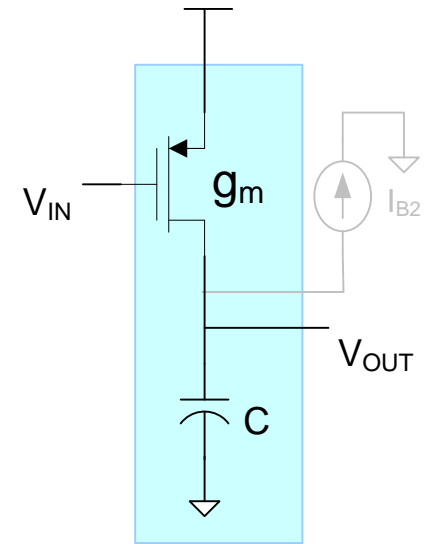
# TA-C Voltage Mode Integrator

Some other perspectives



n-channel input

$$V_{OUT} = \left( \frac{-g_m}{sC} \right) V_{IN}$$



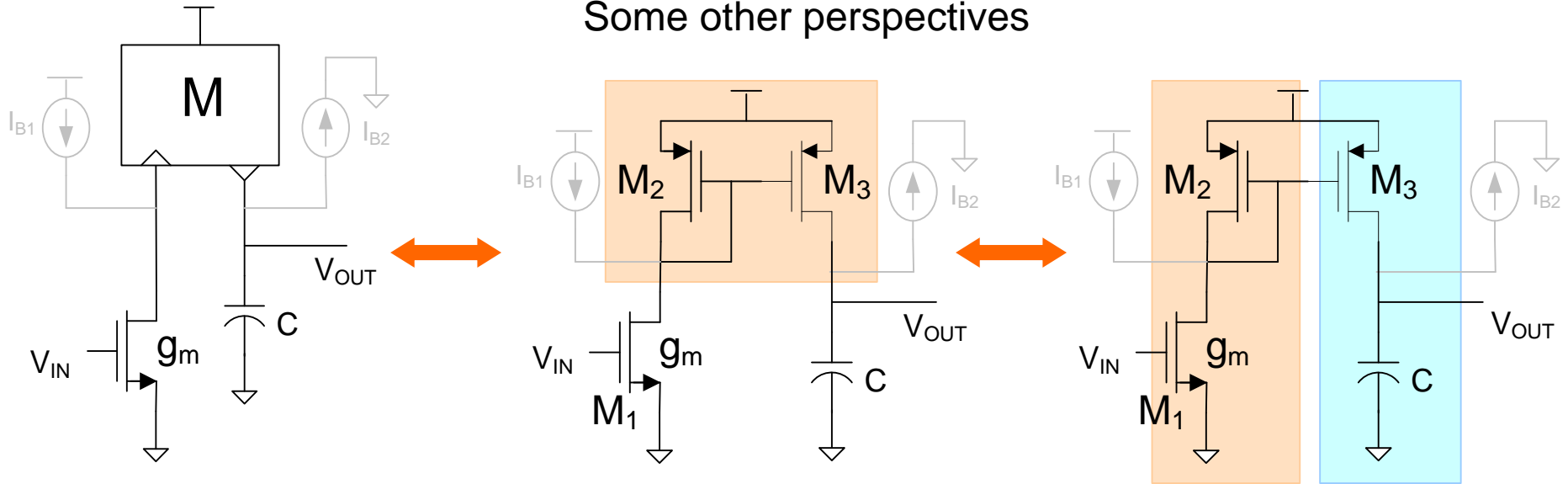
p-channel input

Inverting Integrators



# TA-C Voltage Mode Integrator

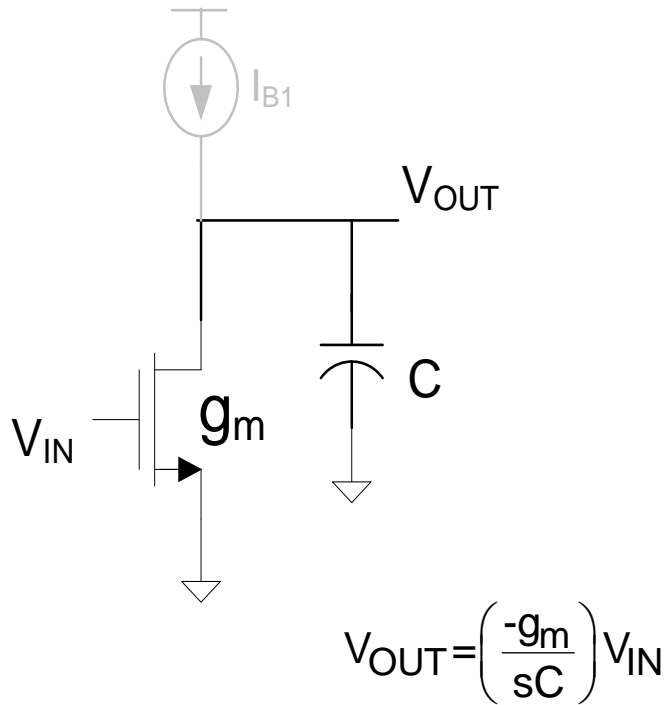
Some other perspectives



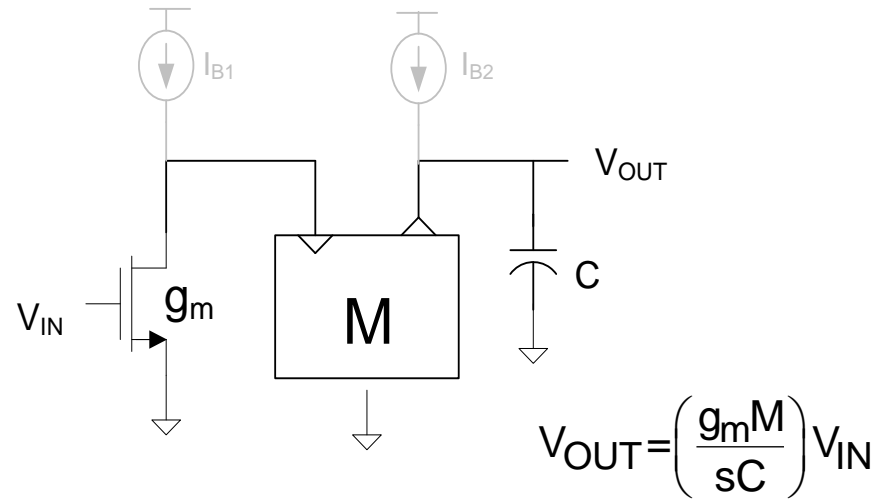
Noninverting Integrator

Can be viewed either as n-channel input with current mirror or as low-gain inverter driving a p-channel input inverting integrator

# TA-C Voltage Mode Integrator



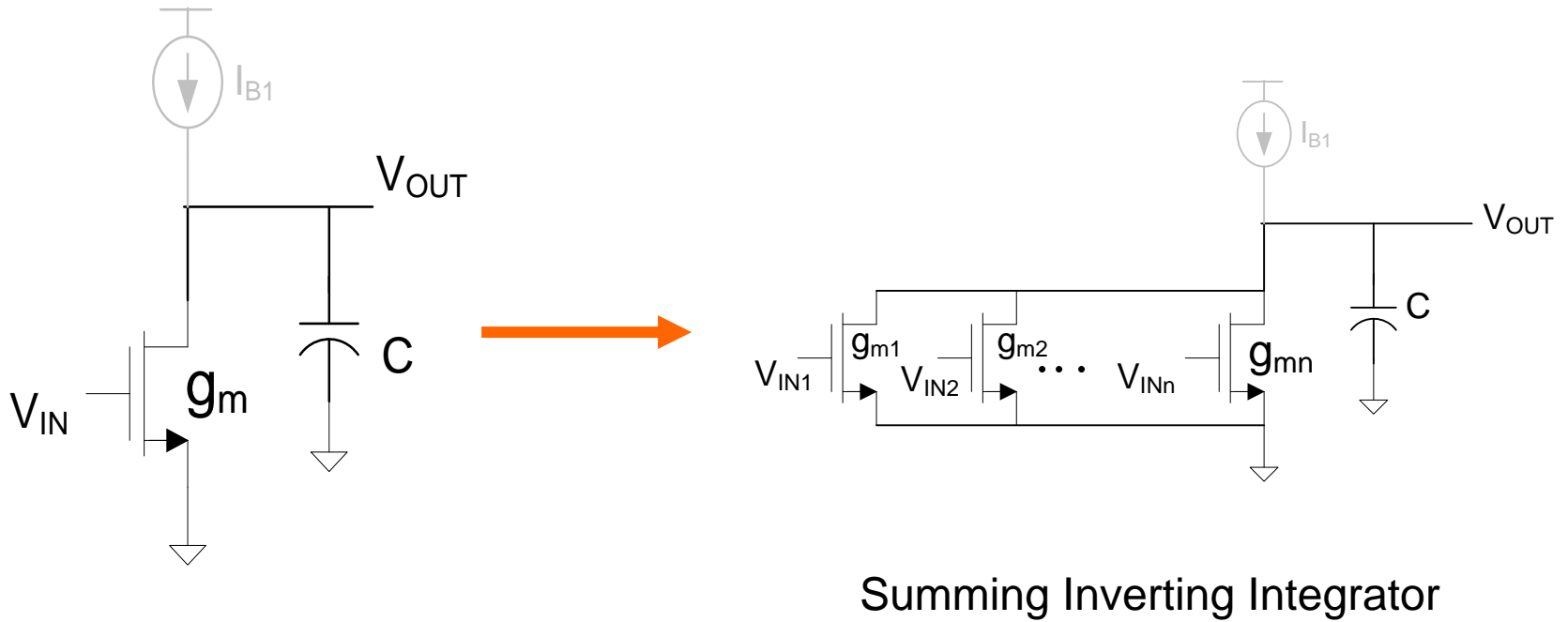
Inverting Integrator



Typically  $M=1$

Alternate noninverting Integrator

# TA-C Voltage Mode Integrator



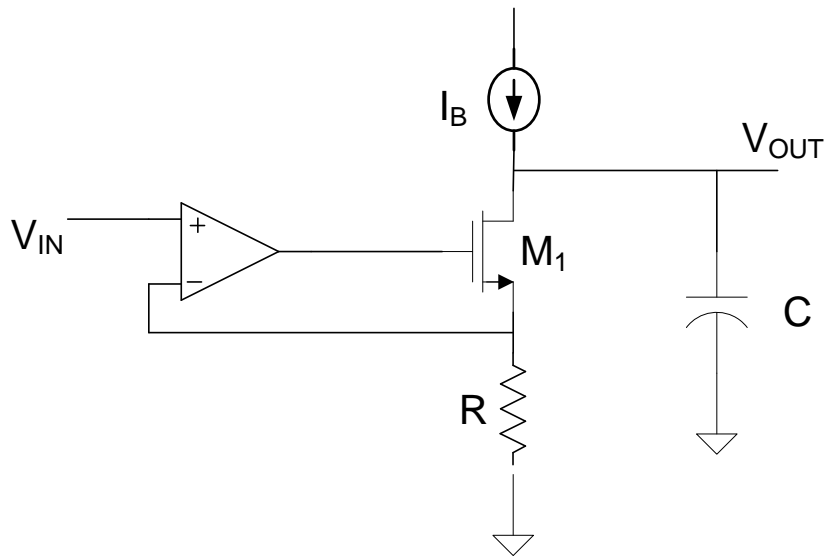
# Voltage Mode Integrators

- Active RC (Feedback-based)
  - MOSFET-C (Feedback-based)
  - OTA-C
  - TA-C
- } Sometimes termed “current mode”

## → Other Continuous-time Structures

- Switched Capacitor
  - Switched Resistor
- } Discrete Time

# Another Voltage Mode Integrator

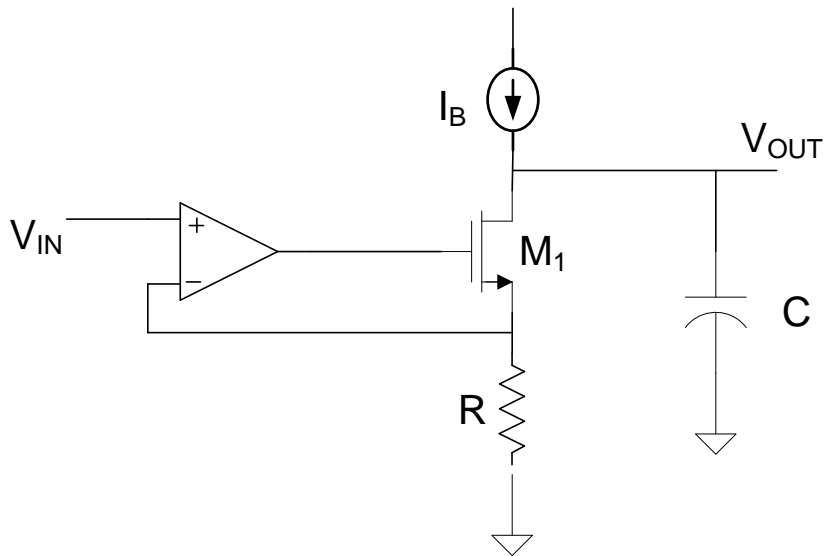


$$V_{OUT} = \left( \frac{-1}{sRC} \right) V_{IN}$$

Inverting Integrator

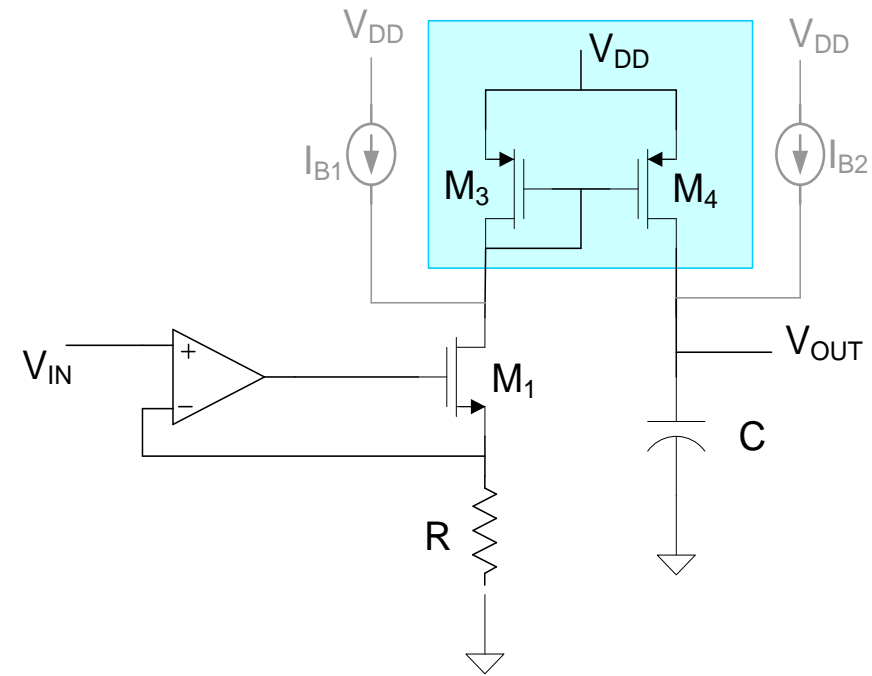
- **Infinite input impedance (in contrast to basic Active RC Integrator)**
- **Both R and C have one terminal grounded**
- **Requires integrated process**
- **Accuracy limited by process and temperature**
- **Size limitations same as basic Active RC Integrator**
- **Limited to lower frequencies because of Op Amp**
- **Good linearity**

# Another Voltage Mode Integrator



Inverting Integrator

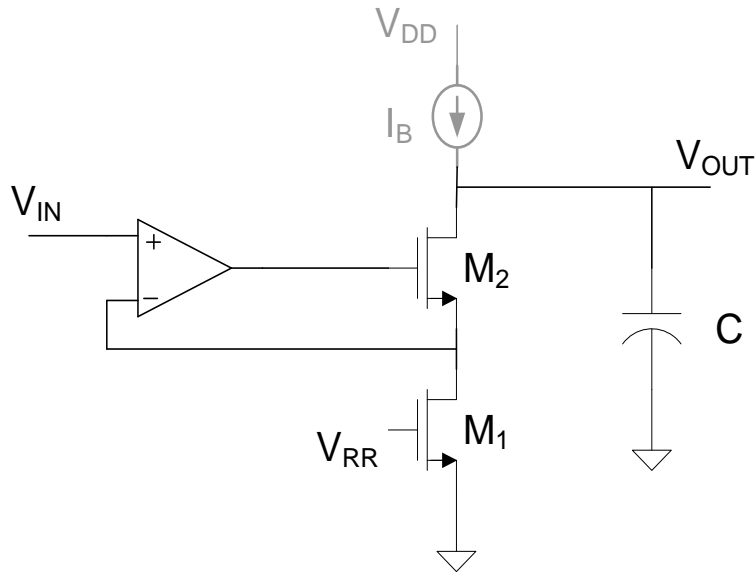
$$V_{OUT} = \left( \frac{-1}{sRC} \right) V_{IN}$$



Noninverting Integrator

$$V_{OUT} = \left( \frac{1}{sRC} \right) V_{IN}$$

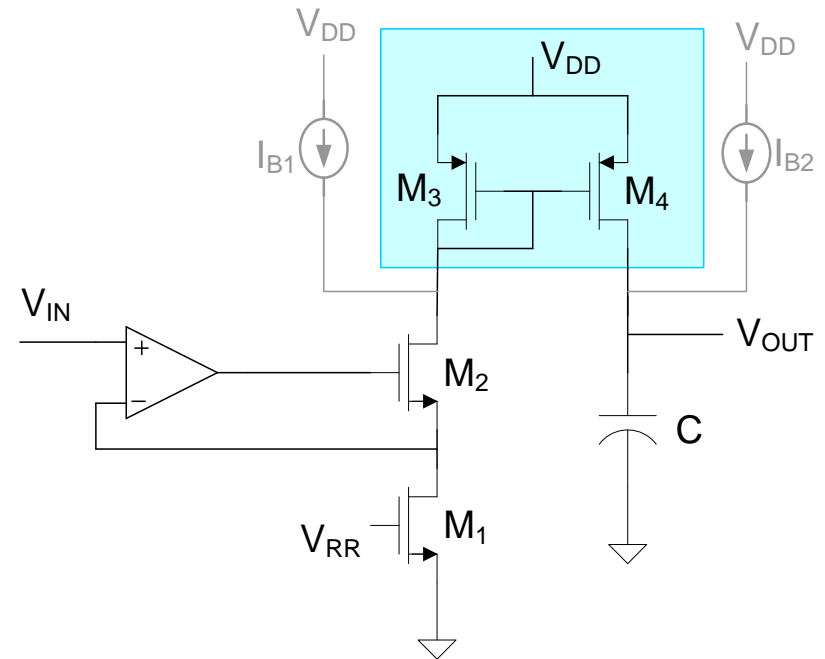
# Another Voltage Mode Integrator



Inverting Integrator

$$V_{OUT} = \left( \frac{-1}{sR_{FET}C} \right) V_{IN}$$

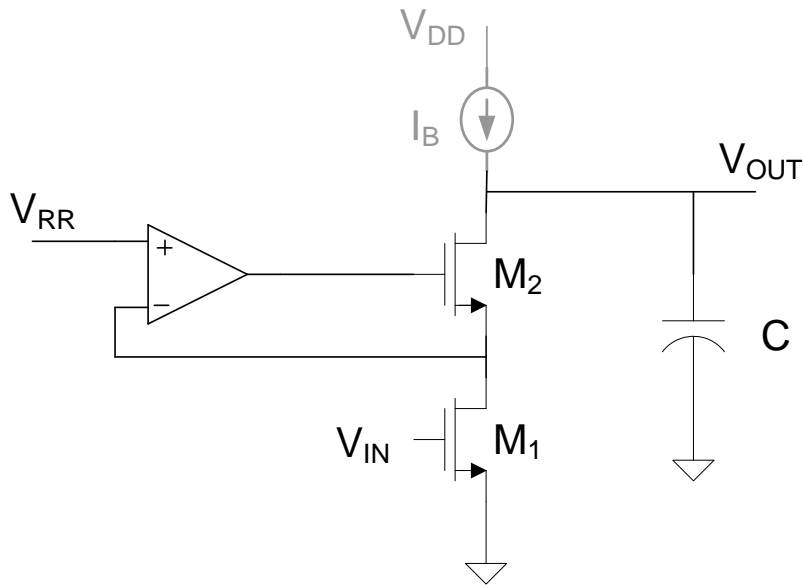
- $M_1$  in triode region
- Reduces Area Concerns but Loss of Linearity
- $I_0$  is programmable with  $V_{RR}$
- Accurate control of  $I_B$  critical



Noninverting Integrator

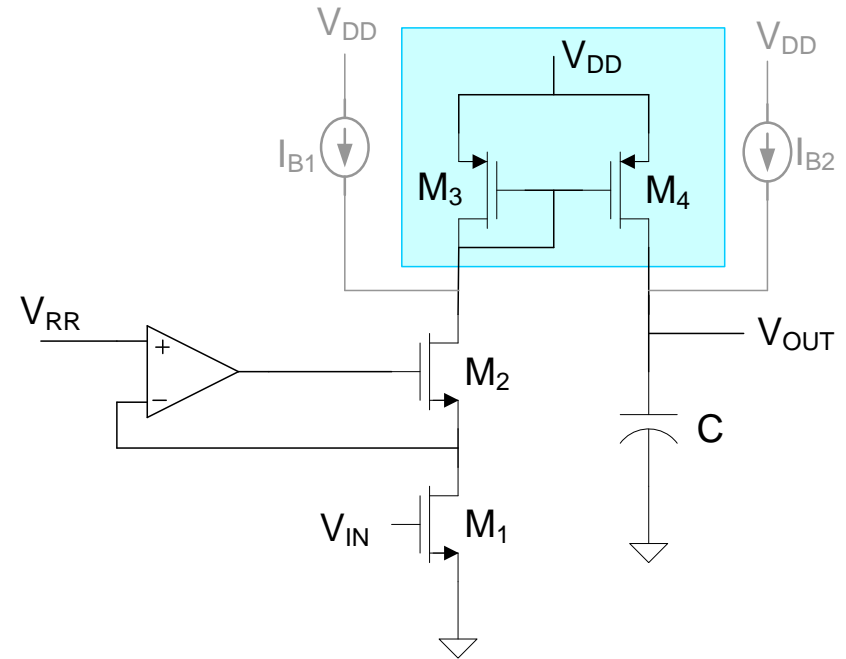
$$V_{OUT} = \left( \frac{1}{sR_{FET}C} \right) V_{IN}$$

# Regulated Cascode Voltage Mode Integrator



Inverting Integrator

$$V_{OUT} = \left( \frac{-g_{mT}}{sC} \right) V_{IN}$$



Noninverting Integrator

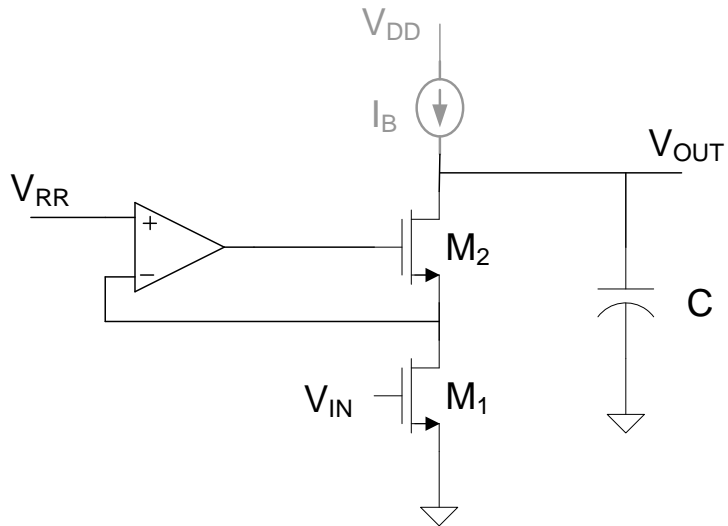
$$V_{OUT} = \left( \frac{g_{mT}}{sC} \right) V_{IN}$$

$g_{mT}$  is triode region transconductance of  $M_1$

- $M_1$  operating in triode region
- $R_{FET}$  programmable with  $V_{RR}$
- Very good linearity properties
- Input impedance still infinite



# Regulated Cascode Voltage Mode Integrator



$$V_{OUT} = \left( \frac{-g_{mT}}{sC} \right) V_{IN}$$

Linearity Properties:

Assuming square-law triode model

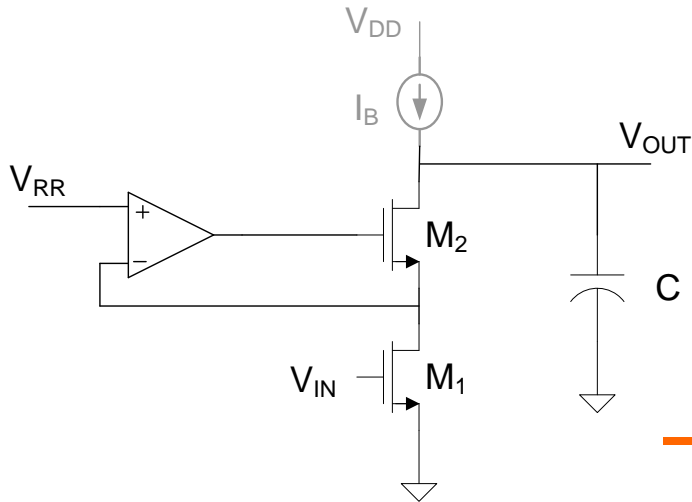
$$I_{D1} = \frac{\mu C_{OX} W}{L} \left( V_{GS} - V_T - \frac{V_{RR}}{2} \right) V_{RR}$$

$$I_{D1} = \left[ \frac{\mu C_{OX} W}{L} V_{RR} \right] V_{IN} + \left[ \frac{\mu C_{OX} W}{L} \left( V_T + \frac{V_{RR}}{2} \right) V_{RR} \right]$$

Note linear dependence on  $V_{IN}$

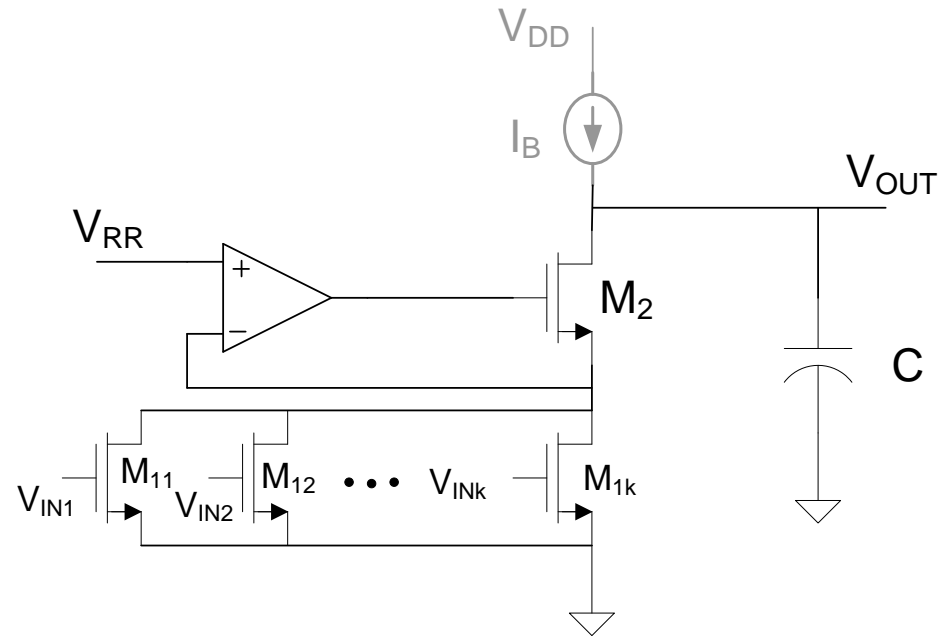
$$g_{mT} = \left[ \frac{L}{\mu C_{OX} W V_{RR}} \right]$$

# Regulated Cascode Voltage Mode Integrator



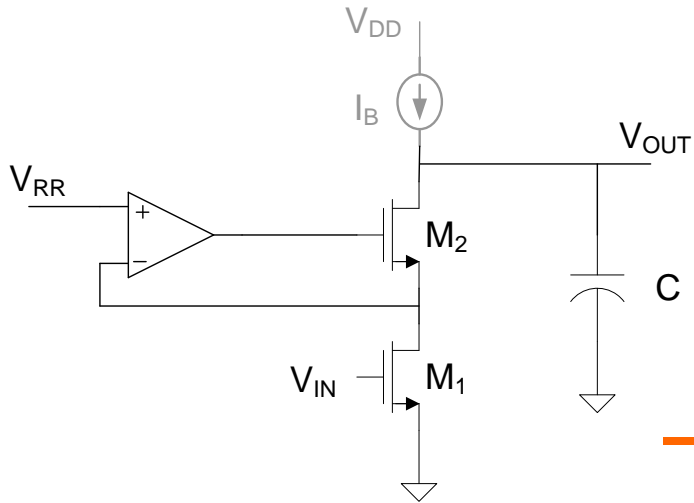
Inverting Integrator

$$V_{OUT} = \left( \frac{-1}{sR_{FET}C} \right) V_{IN}$$



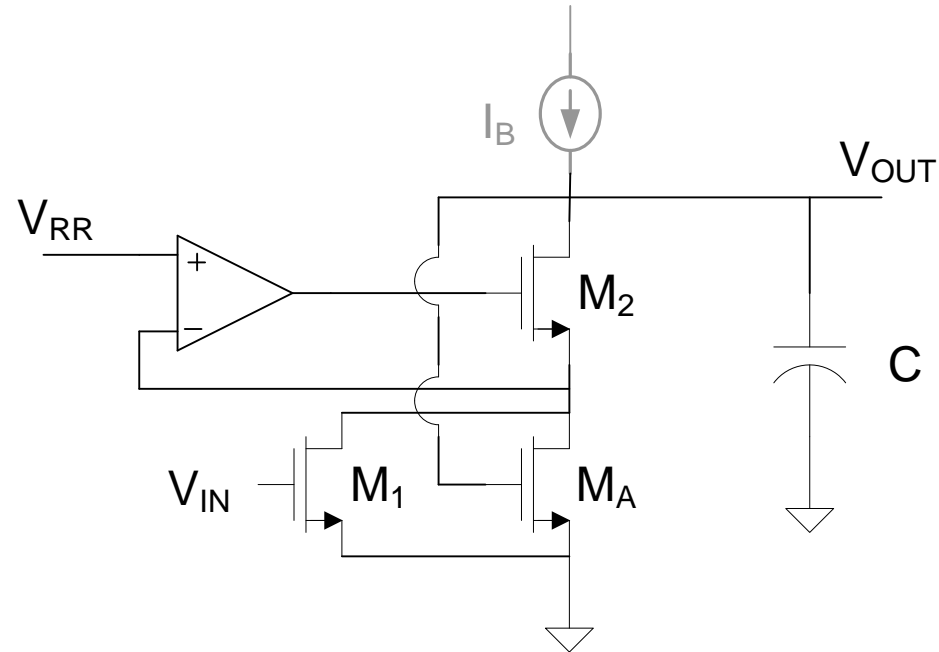
- **Multiple inputs require single additional transistor**
- **Accurate ratioing of gains practical**
- **Can also sum currents on C**

# Regulated Cascode Voltage Mode Integrator



Inverting Integrator

$$V_{OUT} = \left( \frac{-1}{sR_{FET}C} \right) V_{IN}$$



Inverting Lossy Integrator



Stay Safe and Stay Healthy !

**End of Lecture 24**